TTL/Bipolar PROM and RAM Reference Guide booklet V5 - print version
Also a few old CMOS PROMS

Added some modern PROM-pin and -speed compatible CMOS EPROM types, also some military versions, which use a combined NiCr fuselink and CMOS technology.

TS = Tri-State output **OC** = Open Collector output

To be compatible with the rest of the world:
databits OUT now start with D0 (D-zero) and not anymore with O1

Hacked, copied, edited and extended by PE1ABR to give it a decent shape and handy booklet form.

The main part is extracted from
http://www.arlo.net/vectorlist/archive/mbox/November_1997
and in detail: from the text part about the Bipolar PROMs List
See also old reference info at:
http://matthieu.benoit.free.fr/cross/
and the info at
http://antiquetech.com/
http://www.xs4all.nl/~ganswijk/chipdir/n/
Updated with info from:
http://www.spies.com/arcade/ and http://www.citylan.it/wiki/index.php/PROM
also take a look at: http://guru.mameworld.info/

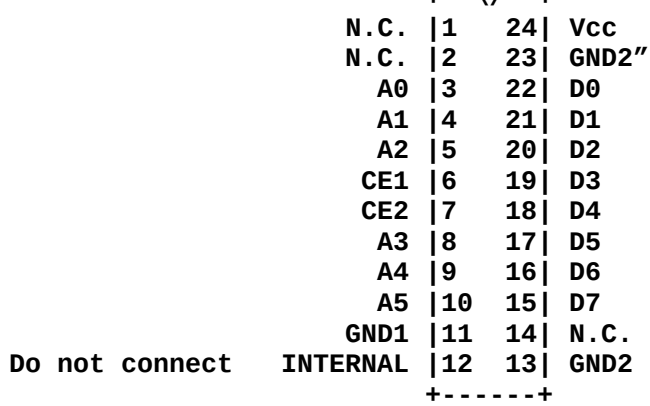
```
                                                          32*8 = 256 bit
                                                            +--\/--+
   NEC       Fujitsu    Intersil   Mitsubishi   OKI       D0 |1   16| Vcc
   ------    -------    --------   ----------   -------   D1 |2   15| /CE
TS uPB410    MB7051     IM5610     M54730       MSL8215   D2 |3   14| A4
           MB7112                                         D3 |4   13| A3
                                                          D4 |5   12| A2
OC uPB400    MB7056     IM5600                MSL8216     D5 |6   11| A1
           MB7111                                         D6 |7   10| A0
                                                          GND|8    9| D7
                                                            +------+
```

	Signetics	MMI	TI	Harris	Raytheon	AMD	National	Intel
TS	82S123 (50ns)	6331-1	TBP18S030	HM7603-5	-	AM27S19	-	-
	82S123A(25ns)	63S081	-	-	-	AM27S19AC	74S288	-
		63LS081				AM27S09		
						AM27LS09	DM8577	
OC	82S23 (50ns)	6330-1	TBP18SA030	HM7602-5	-	AM27S18	-	-
	82S23A(25ns)	63S080	-	-	-	AM27A18AC	74S188	-
		63LS080				AM27S08		
						AM27LS08	DM8578	

Mask programmed custom ROM version: TS= 74S88

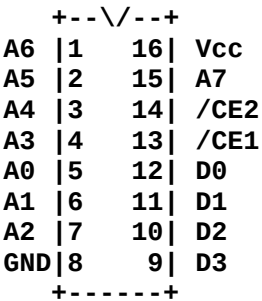
An old military SPECIAL

64*8 = 512 bit



Harris	Landsdale	AMD	National	Intel
HMx-0512-x JAN-0512	HL0512	-	-	-

256*4 = 1024 bit



	NEC	Fujitsu	Fairchild	Intersil	Mitsubishi
TS	uPB423	MB7052 MB7114	93427	IM5623	M54700
OC	uPB403	MB7057 MB7113	93417	IM5603	

	Signetics	MMI	TI	Harris	Raytheon	AMD	National	Intel	OKI
TS	82S129 (50ns) 82S129A(27ns) 82S27	6301-1 63S141	TBP24S10 TBP34S10	HM7611-5 HM7611A	29661 -	27S21 27S21A 27S11	74S287	3621 -	MBL8521A
OC	82S126 (50ns) 82S126A(30ns)	6300-1 63S140	24SA10 -	HM7610-5 HM7610A	29660 -	27S20 27S20A 27S10	74S387 -	3601 -	MBL8520A

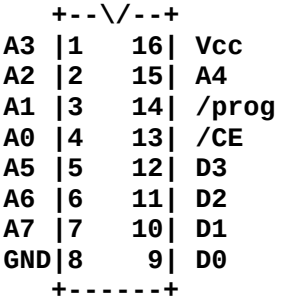
Mask programmed custom ROM version: TS= DM7597 / DM8597 / SN74S187 / 82S229
OC= 82S226

VERY RARE CMOS PROM

This is NO Bipolar PROM
But a TTL compatible CMOS PROM with polysilicon fuses

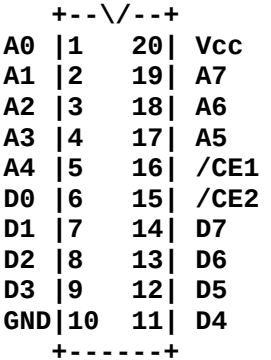
Pinout compatible with CMOS SRAM HM-6562

256*4 = 1024 bit



	Harris
TS	HM-6611

256*8 = 2048 bit



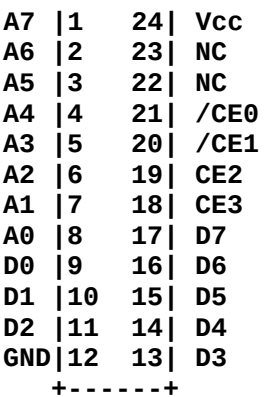
	NEC	Fujitsu
TS	uPB421	MB7118
OC		MB7117

	Signetics	MMI	TI	Harris	Raytheon	AMD	National	Intel
TS	82S135 (45ns)	6309-1	TBP18S22	-	29601	AM27S23	DM74S471	-
	82LS135(100ns)	-	TBP28L22	-	-	-	DM74LS471	-
		63S281	TBP38S22				DM87S221	
		63LS281	SN74S471					
			SN74S571					
OC	-	6308-1	SN74S470		29600		DM74S470	
		63S280					DM74LS470	
		63LS280	TBP38SA22				DM87S222	

Mask programmed custom ROM version: TS= SN74S371, OC= SN74S27

SPECIAL PINNING VERSION

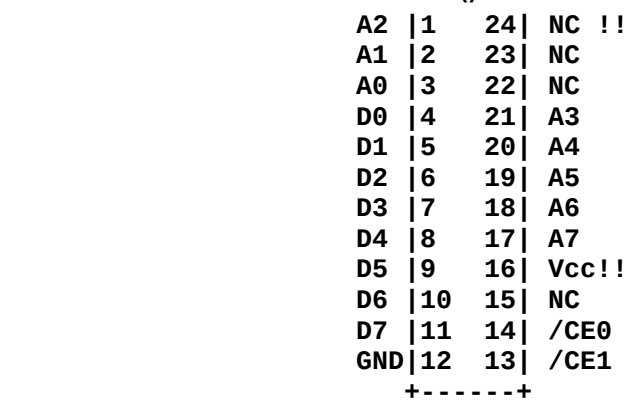
256*8 = 2048 bit



	Signetics	MMI	Harris	Raytheon	AMD	National	Intel
TS	-	6335-1	HM7629-5	-	-	-	-
OC		6336-1	HM7628-5				

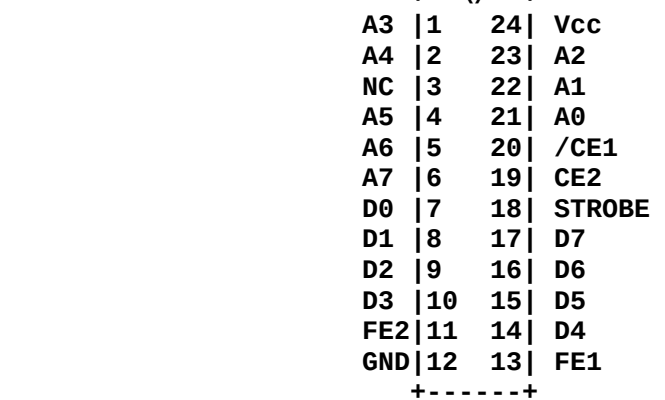
SPECIAL PINNING VERSION

256*8 = 2048 bit



	Signetics	MMI	TI	Harris	Raytheon	AMD	National	Intel
TS	-	63135-1	-	-	-	-	-	-

256*8 = 2048 bit
(latched)



	Signetics	MMI	TI	Harris	Raytheon	AMD	National	Intel
TS	82S114 (60ns)	-	-	-	-	-	-	-

Mask programmed custom ROM version: TS= 82S214 , 8204

512*4 = 1024 bit

```
   +--\/--+
A6 |1   16| Vcc
A5 |2   15| A7
A4 |3   14| A8
A3 |4   13| /CE
A0 |5   12| D0
A1 |6   11| D1
A2 |7   10| D2
GND|8    9| D3
   +------+
```

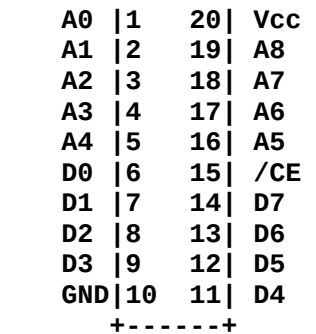
	NEC	Fujitsu	FSC Fairchild	Intersil
TS	uPB412	MB7053	93446	IM56S24
		MB7116		IM5624
OC		MB7058	93436	IM56S04
		MB7115		IM5604

	Signetics	MMI	TI	Motorola Harris	Raytheon	AMD	National	Intel
TS	82S131 (50ns)	6306-1	-	HM7621-5	29611	AM27S13	DM74S571	3622
	82S131A(30ns)	63S241		HM7621A	29613	AM27S13A	DM74S571A	3622A
		63LS241		MCM7621				
OC	82S130 (50ns)	6305-1	-	HM7620-5	29610	AM27S12	DM74S570	3602
	82S130A(33ns)	63S240		HM7620A-5	29612	AM27S12A	DM74S570A	3602A
		63LS240		MCM7620				

Mask programmed custom ROM version: TS= 82S231 / SN74S370
OC= 82S230 / SN74S270

	NEC	Fujitsu
TS	uPB424	MB7124
OC		MB7123

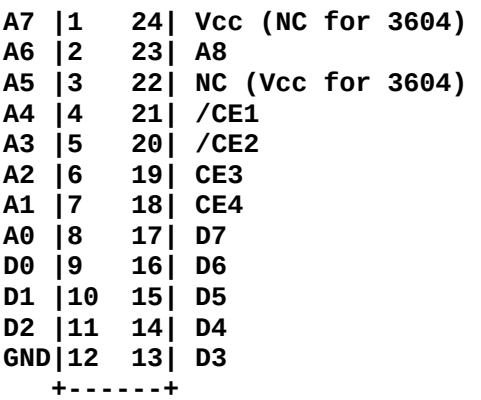
512*8 = 4096 bit



	Signetics	MMI	TI	Motorola Harris	Raytheon	AMD	National	Intel
TS	82S147 (60ns)	6349-1	TBP28S42	HM7649-5	29621	- 27?	DM74S472	-
	82S147A(45ns)	6349-2	TBP28L42	HM7649A-5	29621A	AM27S29	DM74S472A	-
	82HS147		TBP18S42		29623			
	82S147AN/BN	63LS481						
OC	82S146	6348-1	TBP28SA42	HM7648-5	29620	AM27S28	DM74S473	-
		6348-2			29622			
		63LS480						

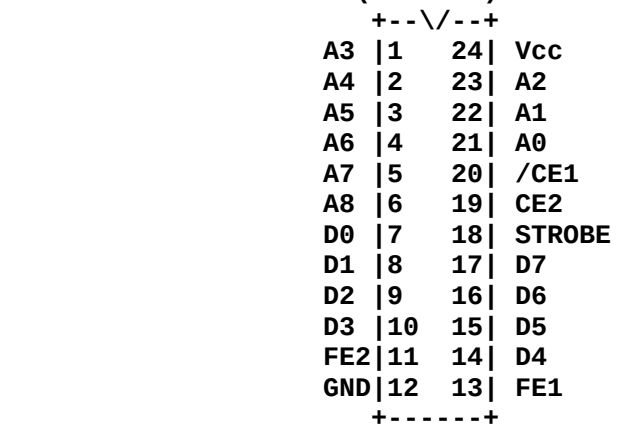
	NEC	Fairchild	Fujitsu	Intersil
TS	uPB425	93448	MB7126	IM5625
OC	uPB405	93438		IM5605

512*8 = 4096 bit



	Signetics	MMI	TI	Motorola Harris	Raytheon	AMD	National	Intel
TS	82S141 (60ns)	6341-1 63S483 63S485	TBP28S46 SN74S474	HM7641-5 MCM7641	29625 29627	AM27S31	DM74S474 87S296	3624
OC	82S140 (60ns)	6340 63S482	TBP28SA46 SN74S475	HM7640-5 MCM7640	29624 29626	AM27S30	DM74S475 87S295	3604

512*8 = 4096 bit
(latched)



Registered / latched PROM

	Signetics	MMI	TI	Harris	Raytheon	AMD	National	Intel
TS	82S115 (60ns)	-	-	7647O 7647R	-	27S15	-	-

Mask programmed custom ROM version: TS= 82S215 , 8205

NO BIPOLAR, but EPROM CMOS registered/clocked PROM

Added to be complete....

```
512*8 = 4096 bit
  (latched)
   +--\/--+
A7 |1   24| Vcc
A6 |2   23| A8
A5 |3   22| /Set
A4 |4   21| /OE
A3 |5   20| /Clr
A2 |6   19| /ES
A1 |7   18| Clock
A0 |8   17| D7
D0 |9   16| D6
D1 |10  15| D5
D2 |11  14| D4
GND|12  13| D3
   +------+
```

	Cypress
TS	CY7C225A (25 / 12 ns)

VERY SPECIAL LATCHED VERSION

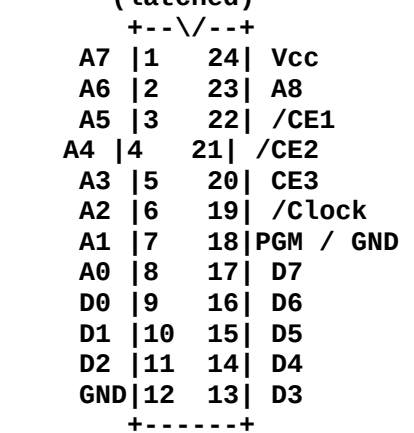
512*8 = 4096 bit (latched)

This is a PROM, but NOT a TTL Bipolar version

It does have the same internal programming technology
= NiCr fuse cell's and NO EPROM cell's
Interfacing = NOT bipolar, but very low power CMOS
Military (handheld) application !!

Registered / latched (synchronous) PROM
Clocked address-in as data-out

MIL-STD/882 VERSION
HM1 = 0.600 Mil (wide)
HM6 = 0.300 Mil (slim)
HM4 = CLCC
B = faster



	Signetics	Intersil	Harris	Raytheon	AMD	Intel
TS	-	HM1-6642/883	-	-	-	-
	-	HM1-6642B/883	-	-	-	-
	-	HM6-6642/883	-	-	-	-
	-	HM6-6642B/883	-	-	-	-
	-	HM4-6642/883	-	-	-	-
		(or HM-6642)				

```
 1024*4 = 4096 bit
    +--\/--+
 A6 |1   18| Vcc
 A5 |2   17| A7
 A4 |3   16| A8
 A3 |4   15| A9
 A0 |5   14| D0
 A1 |6   13| D1
 A2 |7   12| D2
/CE1|8   11| D3
 GND|9   10| /CE2
    +------+
```

	NEC	Fujitsu	FSC Fairchild	Mitsubishi	Intersil Sharp	Hitachi
TS	uPB426	MB7122	93415	M54741A	IM56S26	HN25045
	uPB426-1	MB7134?	93453		LH5626	
	uPB426-2	MB7054				
OC	uPB406	MB7021	93452	M54740A	IM56S06	HN25044
	uPB406-1				LH5606	
	uPB406-2	MB7059				

	Signetics	MMI	TI	Motorola Harris	Raytheon	AMD	National	Intel
TS	82S137 (60ns)	6353-1	TBP24S41	HM7643-5	29641	27S33	74S573	3625
	82S137A(45ns)	63S441	-	HM7643A-5	-	-	74S573A	-
	82S137B(35ns)	63S441A	-	HM7643B-5	-	27S33A	74S573B	-
				MCM7643				
OC	82S136 (60ns)	6352-1	TBP24SA41	HM7642-5		27S32	74S572	3605
		63S440		MCM7642				

(Signetics 8228 is a **NON** compatible custom ROM [1024*4] version with only **16 pins**, no /CE pins, GND shifts to pin 8, further identical pinning sequence)

1024*8 = 8192 bit

```
   +--\/--+
A7 |1   24| Vcc
A6 |2   23| A8
A5 |3   22| A9
A4 |4   21| /CE1
A3 |5   20| /CE2
A2 |6   19| CE3
A1 |7   18| CE4
A0 |8   17| D7
D0 |9   16| D6
D1 |10  15| D5
D2 |11  14| D4
GND|12  13| D3
   +------+
```

	NEC	Fujitsu	Fairchild	Hitachi	Cypress	Intersil
TS	uPB428	MB7132	93451	HN25089	CY7C281	IM5618
		MB7162	93Z451		(EPROM)	
			93L451			
OC	uPB408	MB7131	93450	HN25088		IM5608
			93Z450			
			93L450			

	Signetics		MMI	TI	Motorola Harris	Raytheon	AMD	National	Intel
TS	82S181	70ns	6381-1	TBP28S86	7681-5	29631	27S181	74S478	3628A
	82S181A	55ns	6381-2	28S86-60	7681A	29631A	27S181A	87S181	-
	82S181C	30ns	63S881	-	-	29633	-	87S181A	-
				(24S86 ?)					
OC	82S180	70ns	6380-1	28SA86	7680	29632	27S180	74S479	3608
			63S880		HM7680P-5			87S180	
			also C/LS						

Mask programmed custom ROM version: TS= 82S281 / 27S281 ?
(custom version probably NO inverse CE1 , CE2 ??) OC= 82S280 / 27S280 ?

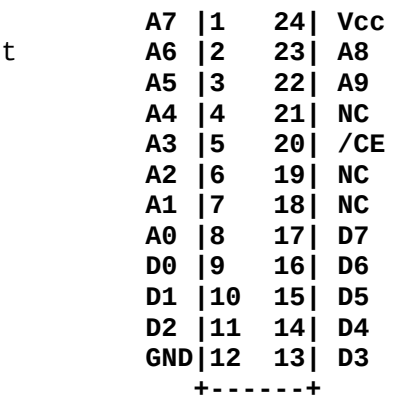
SPECIAL VERSION

1024*8 = 8192 bit

This Prom 82S2708 is a much faster 2708 EPROM replacement

-5, +12 and Vpp NO internal connection

(NEC PROM uPB419 and Harris HM7616 are
2716 EPROM replacements)

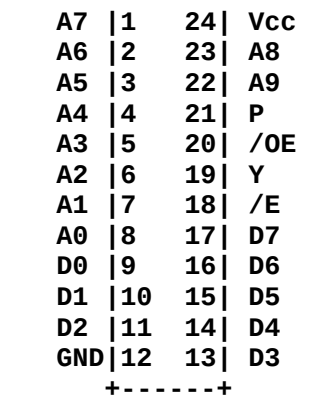


Signetics	TI	NEC	Harris	Fairchild	Motorola	Raytheon
82S2708	TBP28S2708	uPB417	HM7608-5	93461C	MCM82708C MCM82707	29635

MMI	Fujitsu
6384-1	MB7055, MB7060
6385-1	MB7153L, MB7154L

This is NO “TTL Bipolar” PROM - SPECIAL LATCHED EPROM VERSION

1024*8 = 8192 bit



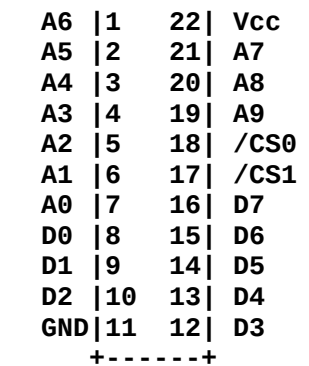
This PROM is CMOS UV erasable EPROM replacement
Addresses and data are internally LATCHED with /E

It allows upgrading to a HM-6716 (double sized) EPROM

This HM-6758 device is NOT equal, but comparable with the Intel 2758 EPROM.
The Intel device is static, it has NO latched feedthrough, 18=/CE, 20=/OE
EPROM 2758 is comparable with the standard 2716

	Intel	Harris	
TS	(2758)	HM-6758H	Y = Vcc
		HM-6758L	Y = Gnd

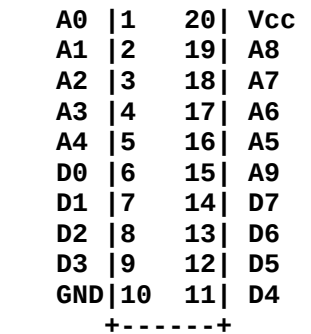
SPECIAL **1024*8 = 8192 bit**



	Fujitsu	MMI
TS	MB7129	6386-1
OC??	MB7132	6387-1

SPECIAL

1024*8 = 8192 bit



	Harris
TS	HM7683-5

OC??

Registered / latched PROM

```
1024*8 = 8192 bit
   (latched)
    +--\/--+
 A7 |1   24| Vcc
 A6 |2   23| A8
 A5 |3   22| A9
 A4 |4   21| /CE1
 A3 |5   20| /CE2
 A2 |6   19| CE3
 A1 |7   18| STROBE
 A0 |8   17| D7
 D0 |9   16| D6
 D1 |10  15| D5
 D2 |11  14| D4
GND |12  13| D3
    +------+
```

	Signetics	Harris	FSC Fairchild
TS	82S183 (60ns)	7681**R**-5	93L451
		=	

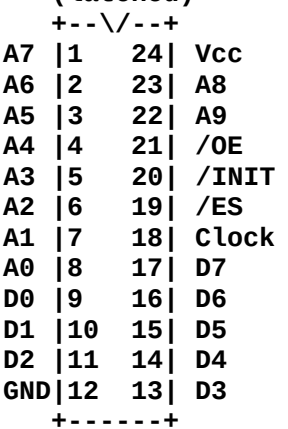
VERY SPECIAL LATCHED VERSION

1024*8 = 8192 bit

(latched)

This is a PROM, but NOT a TTL Bipolar version

This device is a **PROM** with **EPROM** technology



Cypress

CY7C235A 25 / 12 nsec

	NEC	Fairchild	Fujitsu	Hitachi
TS	uPB427	93515	MB7128	HN25085 HN25085S
OC		93514	MB7127	HN25084 HN25084S

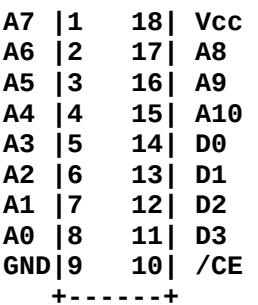
2048*4 = 8192 bit

```
   +--\/--+
A6 |1   18| Vcc
A5 |2   17| A7
A4 |3   16| A8
A3 |4   15| A9
A0 |5   14| D0
A1 |6   13| D1
A2 |7   12| D2
A10|8   11| D3
GND|9   10| /CE
   +------+
```

	Signetics	MMI	TI	Motorola Harris	Raytheon	AMD	National	Intel
TS	82S185(100ns)	6389	-	MCM7685	29651	-	-	-
	82S185A(50ns)	63S841	TBP24S81		29651A	27S185	87S185	-
	82S185B(45ns)	-	24S81-55		29653	27LS185	87S185A	-
OC	82S184(100ns)	6388 63S840	TBP24SA81	MCM7684	29650 29652	27S184 27LS184	87S184	-

SPECIAL PINNING

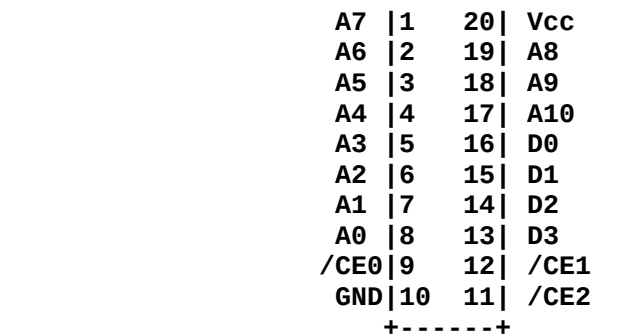
2048*4 = 8192 bit



	Harris
TS	HM7685-5
OC	HM7684-5

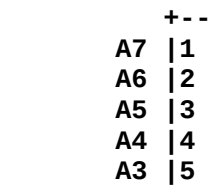
SPECIAL VERSION

2048*4 = 8192 bit



Harris	Motorola
HM7686-5	MCM7686
HM7687-5	

2048*8 = 16384 bit



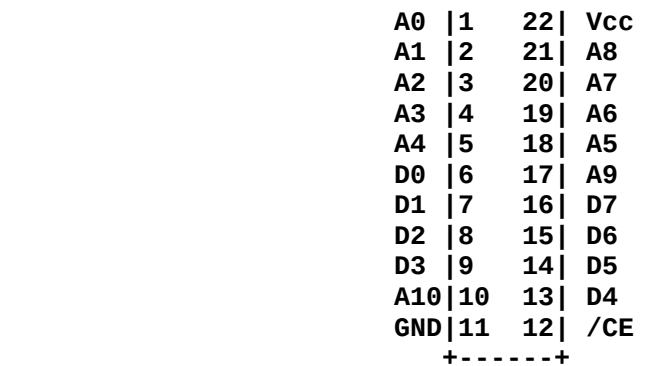
	NEC	FSC Fairchild	Hitachi	Fujitsu	Cypress
TS	uPB429	93511P	HN25169	MB7138	CY7C292
	uPB429-1	93Z511			CY7C292A
	uPB429-2			MBH38 (EPROM?)	(EPROM's) (CY7C291
OC	uPB409	93510	HN25168	MB7137	CY7C293)
	uPB409-1	93Z510			
	uPB409-2				

	WSI	Sharp	ST Thomson
TS	WS57C191C (fast EPROM version)	LH57191	JBP38S165
	WS57C291C (fast EPROM version)		

	Signetics	MMI	TI	Motorola Harris	Raytheon	AMD	National	Intel
TS	82S191(80ns)	-	28S166	76161	29681	-	87S191	3636B
	82S191A(55ns)	63S1681	28S166-55	76161A	29681A	27S191 /291	87S191A	-
	82S191C(35ns)	-	38S165 (EPROM ?)	-	-	27S191A	87S291 87S193	-
OC	82S190(80ns)	63S1680	28SA166	76160	29683	27S190	87S190 87S290	3606

SPECIAL VERSION

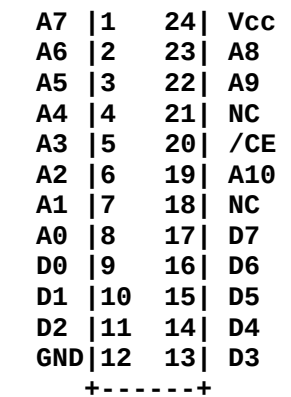
2048*8 = 16384 bit



	Fujitsu
TS	MB7136
OC	MB7135

SPECIAL VERSION

2048*8 = 16384 bit



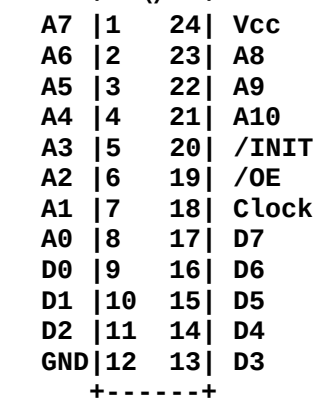
This prom is a much faster 2716 EPROM replacement

(NEC PROM uPB417 and Harris HM7608-5 are
2708 EPROM replacements)

Harris	NEC
HM7616-5	uPB419

VERY SPECIAL LATCHED VERSION

2048*8 = 16384 bit
(latched)



This prom device looks like a registered (= LATCHED)
2716 EPROM replacement **10x faster**
But PIN A10 DIFFERENT !

	AMI	Waferscale Integration Inc WSI	Cypress
TS	AM27S45	WS57C45	CY7C245 CY7C245A

(PROM-speed compatible EPROM versions)

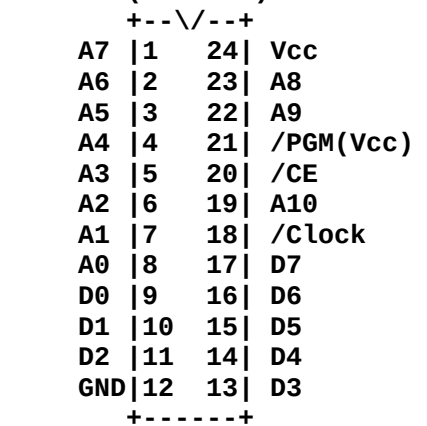
VERY SPECIAL LATCHED VERSION

This is a PROM, but NOT a TTL Bipolar version
It does have the same internal programming technology
= NiCr fuse cell’s and NO EPROM cell’s
Interfacing = NOT bipolar, but very low power CMOS
Military (handheld) application !!

Registered / latched (synchronous) PROM
Clocked address-in as data-out

MIL-STD/882 VERSION

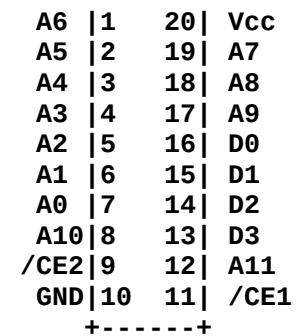
2048*8 = 16384 bit
(latched)



	Signetics	Intersil	Harris	Raytheon	AMD	Intel
TS	-	HM1-6617/883 (or HM-6617)	-	-	-	-

SPECIAL VERSION

4096*4 = 16384 bit



	Fujitsu
TS	MB7134
OC	MB7133

	Fujitsu	NEC	FSC Fairchild
TS	MB7152	uPB430 uPB431	93513
OC			

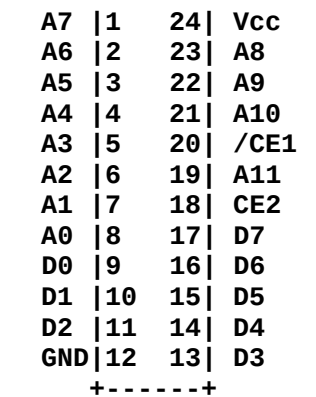
4096*4 = 16384 bit

```
   +--\/--+
A8 |1   20| Vcc
A7 |2   19| A9
A6 |3   18| A10
A5 |4   17| A11
A4 |5   16| /CE1
A3 |6   15| /CE2
A2 |7   14| D0
A1 |8   13| D1
A0 |9   12| D2
GND|10  11| D3
   +------+
```

	Signetics	MMI	TI	Motorola Harris	Raytheon	AMD	National
TS	82S195 (45ns)	63S1641	-	76165-5	-	AM27S41	87S195
	82S195A(35ns)	63S1641A		-	-	AM27S41A	87S195A
	82S195B(25ns)		-	-	-	-	-
OC		63S1640 63LS1640				AM27S40 AM27S40A	

	Fujitsu	Waferscale Integration Inc WSI	ICT
TS	MB7141	WS57C43C	27CX321
			27CX322
	MB7142	(PROM-speed compatible EPROM	
		version but pin's **NOT** 2732 compatible)	
		10 times EPROM speed !	

4096*8 = 32768 bit

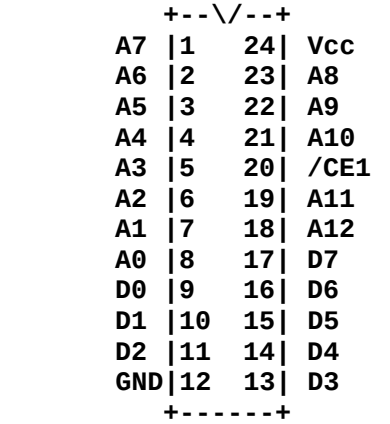


	Signetics	MMI	TI	Harris	Raytheon	AMD	National	Intel
TS	82S321 (70ns)	-	-	76321-5	29671	-	-	3632
	82HS321(45ns)	63S3281	-	-	29671A	AM27S43	87S321	-
	82HS321A(35ns)	63S3281A	-	-	-	AM27S43A	87S421	-
	82HS321B(30ns)	-	-	-	-	-	-	-

	Fujitsu	Lattice	Cypress	Fairchild	National
TS	MB7143	PR64K8	CY7C261	93Z565	93Z665C
	MB7144		CY7C263	93Z565A	93Z667C
			CY7C264	-	
			(EPROM's)		

	Waferscale Integration Inc WSI	ICT	Sharp	NEC
TS	WS57C49C	27CX641	SH5762	27HC65
	(EPROM)	27CX642		

8192*8 = 65536 bit

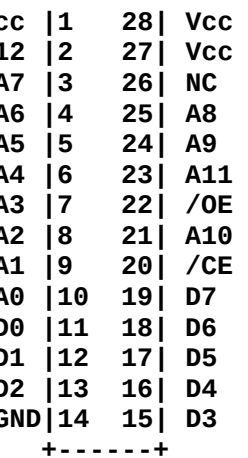


	Signetics	MMI	TI	Harris	Raytheon	AMD	SSI
TS	82S641 (55ns)	-	TMS27PC49	76641-5	-	AM27S49	SS1203
	82S641A(45ns)	-	-	76641A-5	-	AM27S49A	
	82S641B(35ns)	-	-	-	-	-	-

(Some devices are PROM-pin AND -speed compatible EPROM's – **10x faster EPROM**)

VERY SPECIAL VERSION with POWER DOWN mode on CE pin

8192*8 = 65536 bit



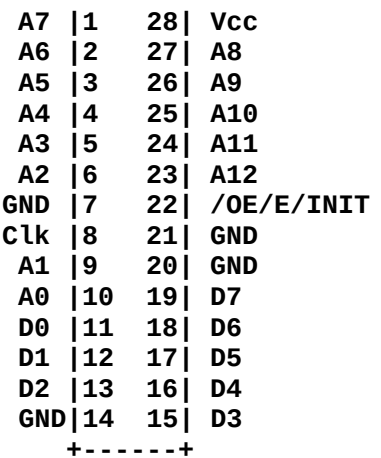
Cypress

CY7C266

NO Bipolar device, but fast EPROM technology

VERY SPECIAL LATCHED VERSION

8192*8 = 65536 bit

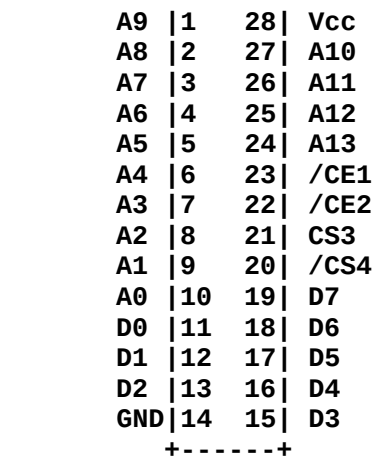


Cypress

CY7C265

NO Bipolar device, but fast EPROM technology

16384*8 = 131072 bit



Waferscale
Integration Inc
WSI

TS WS57C51C
(=EPROM version)

Philips/NXP Signetics	AMD	Cypress	Sharp
TS 82HS1281	27S51	CY7C251 CY7C253 CY7C254 (EPROM)	LH57127

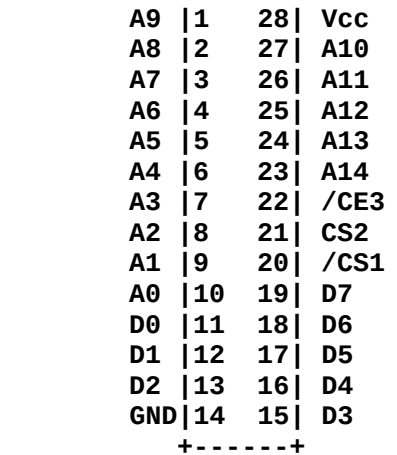
(Some devices are PROM-pin AND -speed compatible EPROM’s – **10x faster EPROM**)

Waferscale
Integration Inc
WSI

TS WS57C71C

(= fast EPROM version)

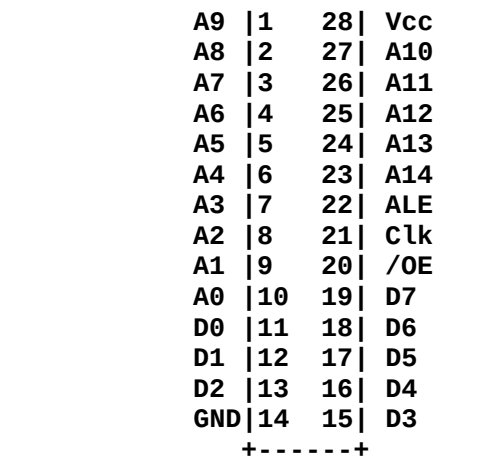
32768*8 = 262144 bit



(This device is a PROM in EPROM technology – **10x faster EPROM**)

VERY SPECIAL LATCHED VERSION

32768*8 = 262144 bit



Cypress

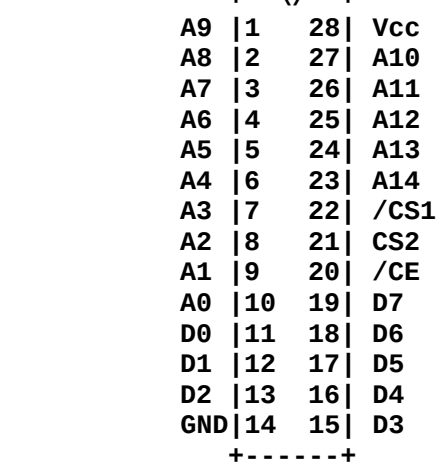
CY7C277

(fast EPROM version)

(This device is a PROM in EPROM technology – **10x faster EPROM**)

VERY SPECIAL VERSION with POWER DOWN mode on CE pin

32768*8 = 262144 bit



Cypress

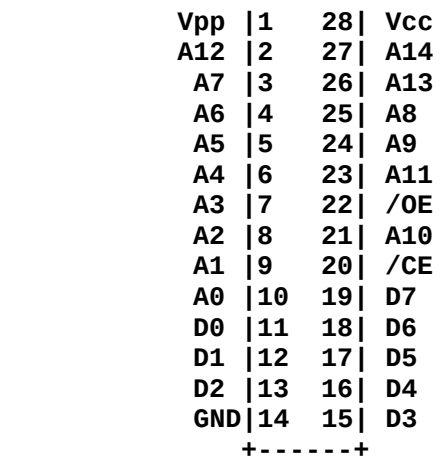
CY7C271 300 mil

(fast EPROM version)

(This device is a PROM in EPROM technology – **10x faster EPROM**)

VERY SPECIAL VERSION with POWER DOWN mode on CE pin

32768*8 = 262144 bit



Cypress

CY7C274 600 mil

(fast EPROM version)

(This device is a PROM in EPROM technology – **10x faster EPROM**)

FOLLOWING NOW: **TTL (ONLY) bipolar RAM**

THIS IS NOT A PROM

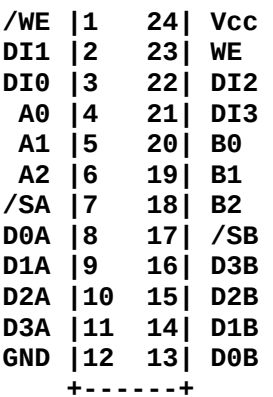
This is a **RAM**, it’s here to be complete and not confused

Dual output port memory (single port IN)

A0 – A3 = read / write addresses port A
B0 – B3 = read only addresses port B

DIx = data IN
DxA = data port A out
DxB = data port B out
/SA = enable output A
/SB = enable output B

8*4 = 32 bit



	Philips/NXP Signetics	MMI	TI	Harris	Motorola Fairchild Raytheon	AMD	National	Intel
TS	82S112	-	-	-	-	-	-	-
OC	82S12	-	-	-	-	-	-	-

THIS IS NOT A PROM

This is a **RAM**, it's here to be complete and not confused
Standard **INVERSE** data OUT, except ***1)**

16*4 = 64 bit

```
   +--\/--+
A0 |1   16| Vcc
/CE|2   15| A1
/WE|3   14| A2
I0 |4   13| A3
/D0|5   12| I3
I1 |6   11| /D3
/D1|7   10| I2
GND|8    9| /D2
   +------+
```

	NEC	Motorola
TS	uPB2089	-
OC	uPB2289	4064

	Philips/NXP Signetics	MMI	TI	Harris	Fairchild Raytheon	AMD	National	Intel
TS	74S89 74F189 74F219 ***1)**	6561 5561	74S189	-	93405	AM27S03 AM27S07 ***1)**	-	-
OC	82S25	6560 5560	74S289 74LS289	-	93404	AM27S02 AM27LS02	-	3101A

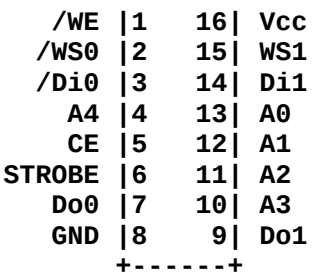
***1) AM27S07 and 74F219 = NON-inverting outputs D0 – D3 and NOT /D0 – /D3**

THIS IS NOT A PROM

This is a **RAM**, it’s here to be complete and not confused

2 BIT Dual port Stack memory

32*2 = 64 bit

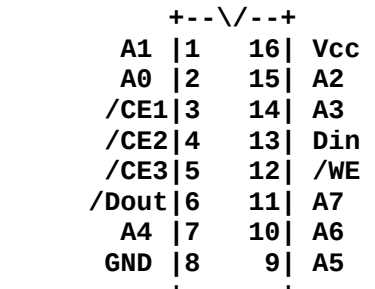


	Philips/NXP Signetics	MMI	TI	Harris	Fairchild Raytheon	AMD	National	Intel
TS								
OC	82S21							

THIS IS NOT A PROM

This is a RAM, it’s here to be complete and not confused

256*1 = 256 bit



	NEC	Motorola	Hitachi
TS	uPB2202	4256	-
OC	uPB2206	-	HM2504

	Philips/NXP Signetics	MMI	TI	Harris	Fairchild Raytheon	AMD	National	Intel
TS	82S16 82S116 (82S06 TTL) 82LS16	6531	74S200 74S201 TTL	-	93421	AM27S00 AM27LS00	-	P3106
OC	82S17 82S117 (82S07 TTL) 82LS17	- -	74S301 TTL (74S206 TTL) 74LS301	-	93411	AM27S01 AM27LS01	-	P3107

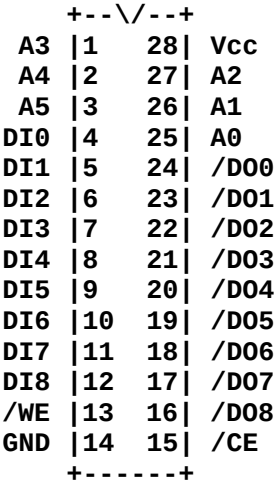
THIS IS NOT A PROM

This is a **RAM**, it’s here to be complete and not confused

Stack or FIFO memory (with parity or flag)

OUTPUT IS THE COMPLEMENT OF THE INPUT DATA !!
(= inverted !)

64*9 = 576 bit

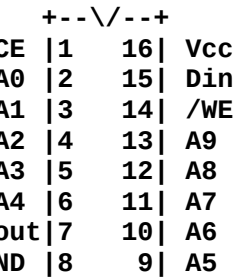


	Philips/NXP Signetics	MMI	TI	Harris	Motorola Fairchild Raytheon	AMD	National	Intel
TS	-	-	-	-	-	-	-	-
OC	82S09 82S19	-	-	-	-	-	-	-

THIS IS NOT A PROM

This is a RAM, it’s here to be complete and not confused

1024*1 = 1024 bit



	NEC	Intersil	Hitachi
TS	-	5518	HM2511
OC	-	5508	HM2510

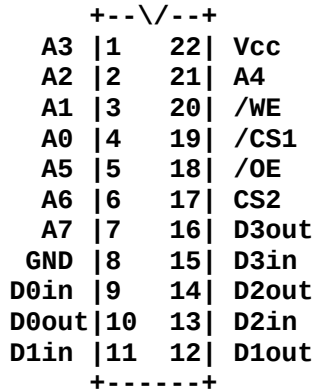
	Philips/NXP Signetics	MMI	TI	Harris	Motorola Fairchild Raytheon	AMD	National	Intel
TS	82S11	-	74S209	-	93425A	-	-	-
OC	82S10	-	74S309	-	93415A	-	-	3110??

THIS IS NOT A PROM

This is a **RAM**, it’s here to be complete and not confused

Stack memory

256*4 = 1024 bit



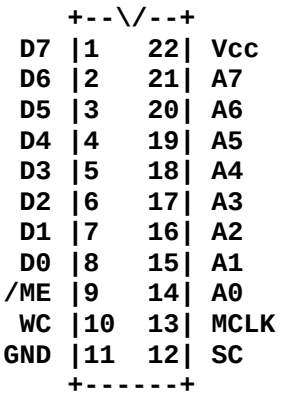
	Philips/NXP Signetics	MMI	TI	Harris	Motorola Fairchild Raytheon	AMD	National	Intel
TS		-		-	93L422	-	-	-

THIS IS NOT A PROM

This is a **RAM**, it’s here to be complete and not confused

Stack memory for 8X300 or 8X305 CPU
Latched

256*8 = 2048 bit



	Philips/NXP Signetics	MMI	TI	Harris	Motorola Fairchild Raytheon	AMD	National	Intel
TS	8X350	-	-	-	-	-	-	-
OC	X							

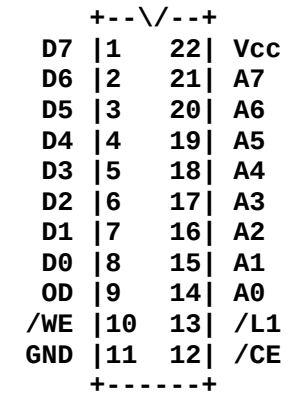
THIS IS NOT A PROM

This is a **RAM**, it’s here to be complete and not confused

Latched Stack or FIFO memory

Latched and UN-latched mode

256*8 = 2048 bit



	Philips/NXP Signetics	MMI	TI	Harris	Motorola Fairchild Raytheon	AMD	National	Intel
TS	82S208	-	-	-	-	-	-	-
OC	X							

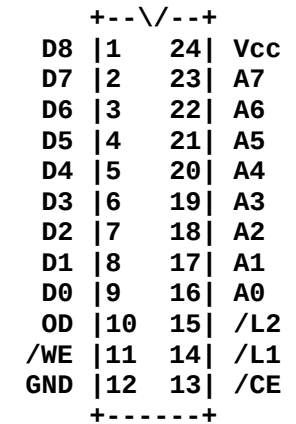
THIS IS NOT A PROM

This is a **RAM**, it’s here to be complete and not confused

Latched Stack or FIFO memory (with parity or flag)

Latched and UN-latched mode

256*9 = 2304 bit



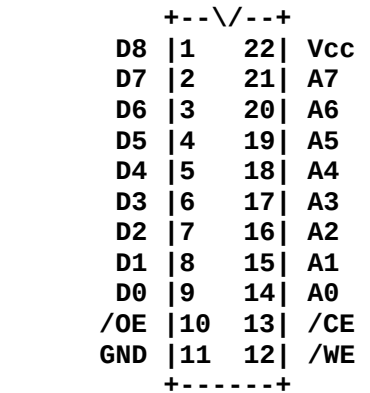
	Philips/NXP Signetics	MMI	TI	Harris	Motorola Fairchild Raytheon	AMD	National	Intel
TS	82S210	-	-	-	-	-	-	-
OC	X	-	-	-	-	-	-	-

THIS IS NOT A PROM

This is a **RAM**, it’s here to be complete and not confused

Stack or FIFO memory (with parity or flag)

256*9 = 2304 bit



	Philips/NXP Signetics	MMI	TI	Harris	Motorola Fairchild Raytheon	AMD	National	Intel
TS	82S212	-	-	-	-	-	-	-
OC	X	-	-	-	-	-	-	-