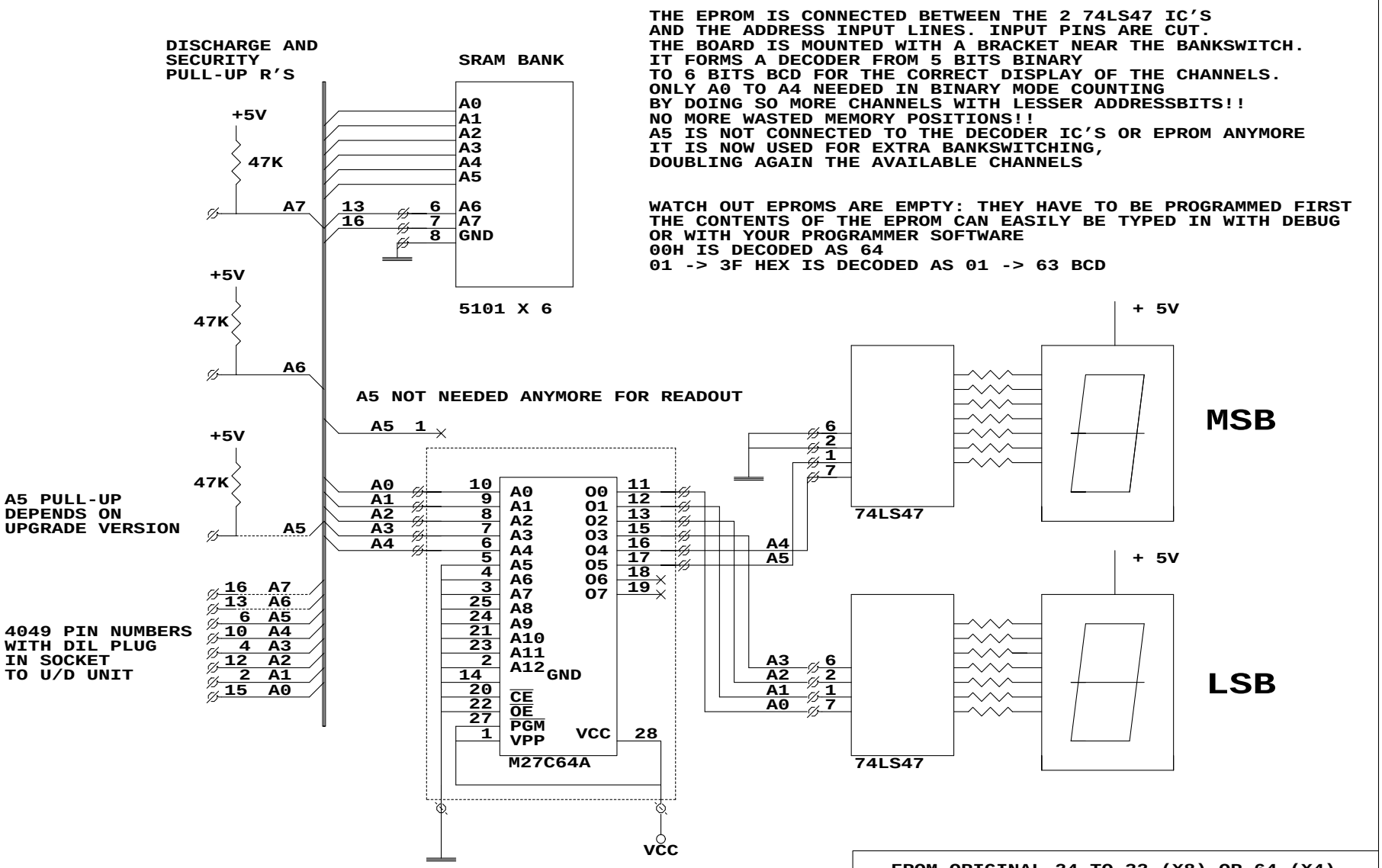
DISCHARGE AND
SECURITY
PULL-UP R'S
SRAM BANK
A0
A1
A2
A3
A4
A5
A6
A7
GND
+5V
47K
5101 X 6
THE EPROM IS CONNECTED BETWEEN THE 2 74LS47 IC'S
AND THE ADDRESS INPUT LINES. INPUT PINS ARE CUT.
THE BOARD IS MOUNTED WITH A BRACKET NEAR THE BANKSWITCH.
IT FORMS A DECODER FROM 5 BITS BINARY
TO 6 BITS BCD FOR THE CORRECT DISPLAY OF THE CHANNELS.
ONLY A0 TO A4 NEEDED IN BINARY MODE COUNTING
BY DOING SO MORE CHANNELS WITH LESSER ADDRESSBITS!!
NO MORE WASTED MEMORY POSITIONS!!
A5 IS NOT CONNECTED TO THE DECODER IC'S OR EPROM ANYMORE
IT IS NOW USED FOR EXTRA BANKSWITCHING,
DOUBLING AGAIN THE AVAILABLE CHANNELS
WATCH OUT EPROMS ARE EMPTY: THEY HAVE TO BE PROGRAMMED FIRST
THE CONTENTS OF THE EPROM CAN EASILY BE TYPED IN WITH DEBUG
OR WITH YOUR PROGRAMMER SOFTWARE
00H IS DECODED AS 64
01 -> 3F HEX IS DECODED AS 01 -> 63 BCD
+ 5V
MSB
LSB
74LS47
A5 NOT NEEDED ANYMORE FOR READOUT
A5 PULL-UP
DEPENDS ON
UPGRADE VERSION
4049 PIN NUMBERS
WITH DIL PLUG
IN SOCKET
TO U/D UNIT
M27C64A
O0
O1
O2
O3
O4
O5
O6
O7
CE
OE
PGM
VPP
VCC
EPROM IN SOCKET ON SMALL CIRCUIT BOARD
FROM ORIGINAL 24 TO 32 (X8) OR 64 (X4)
Title
CORRECTING DISPLAY READOUT NDH-518 UPGRADE
Size
A
Document Number
made by W. Geeraert PE1ABR
REV
3
Date: April 22, 2000
Sheet 1 of 1