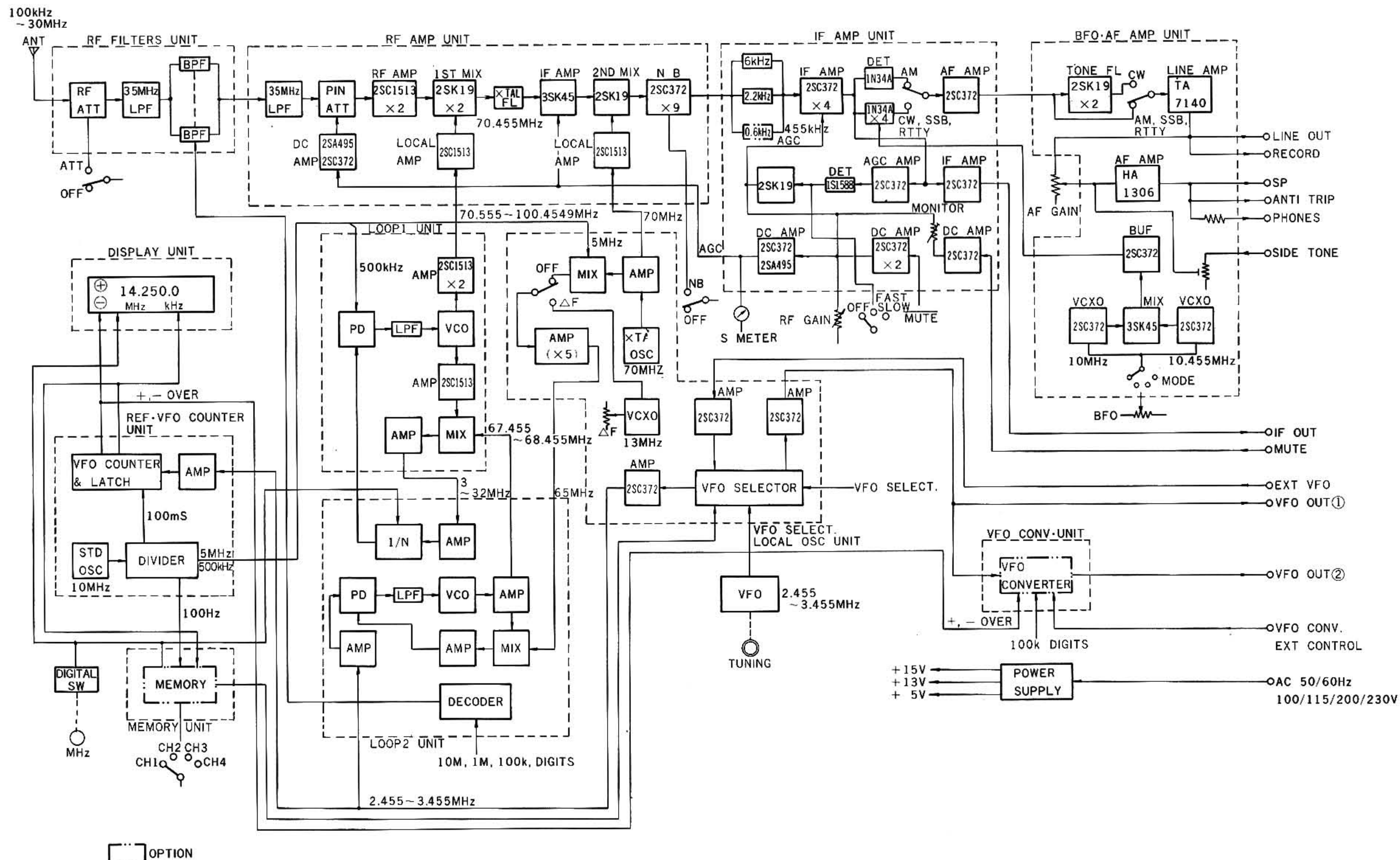
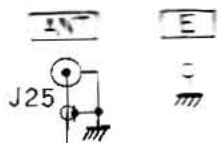
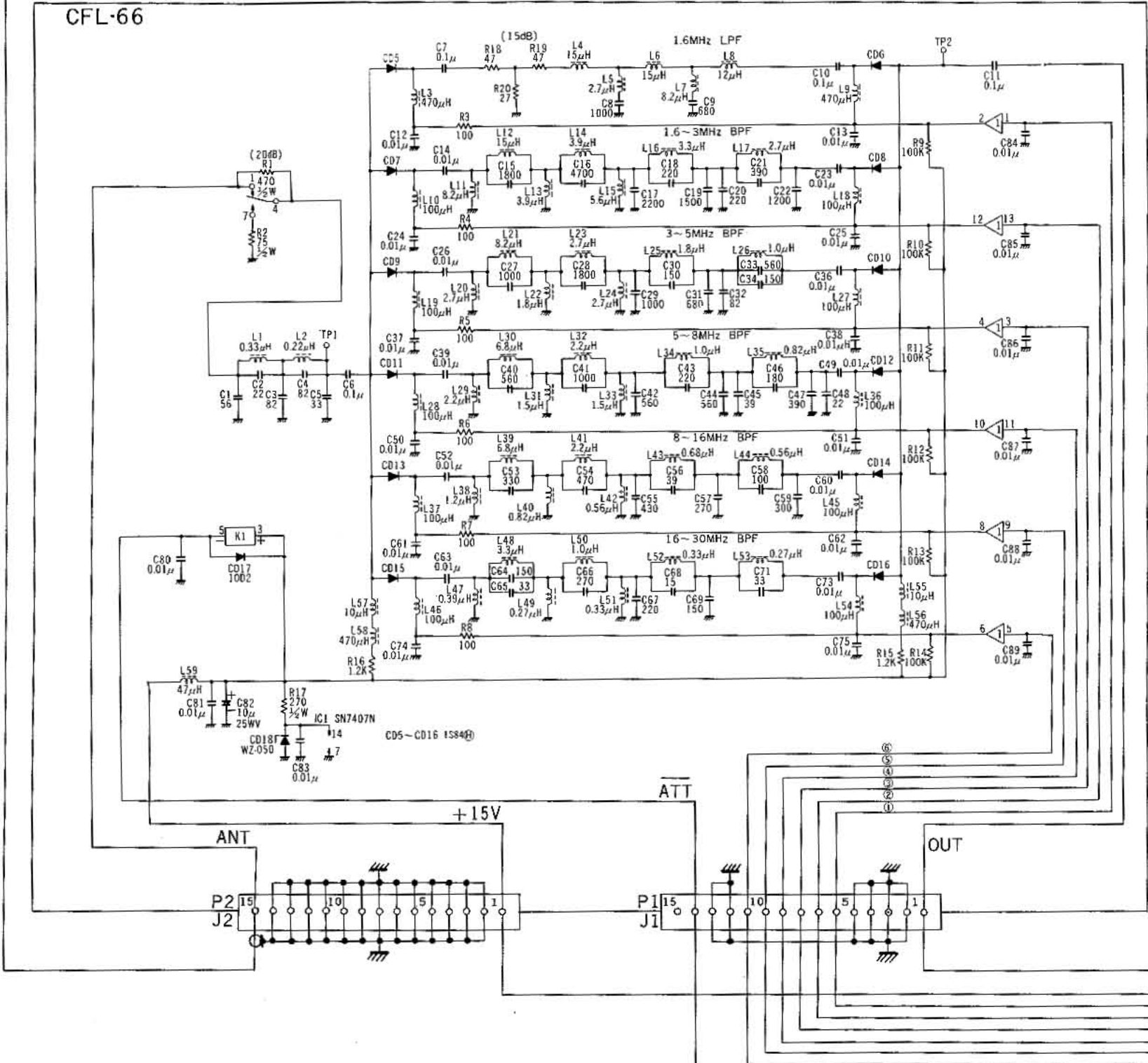




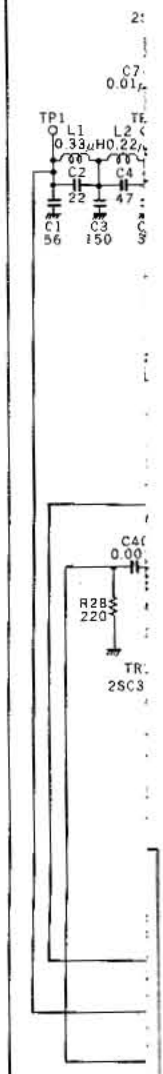
APPENDIX 1 MODEL NRD-505 ALL-WAVE RECEIVER FUNCTIONAL BLOCK DIAGRAM



RF INPUT FILTERS UNIT
CFL-66

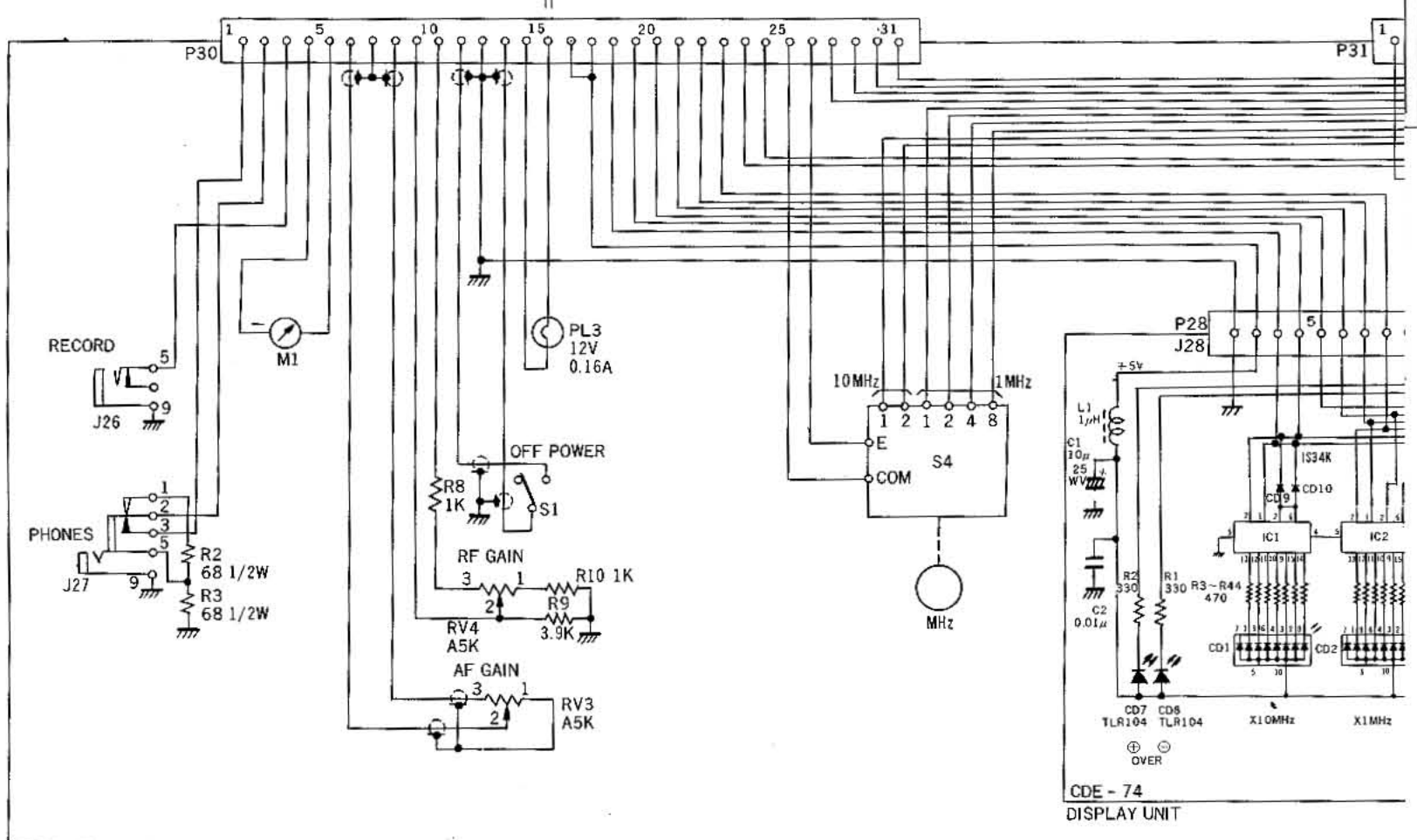


RF AM
CAF-66

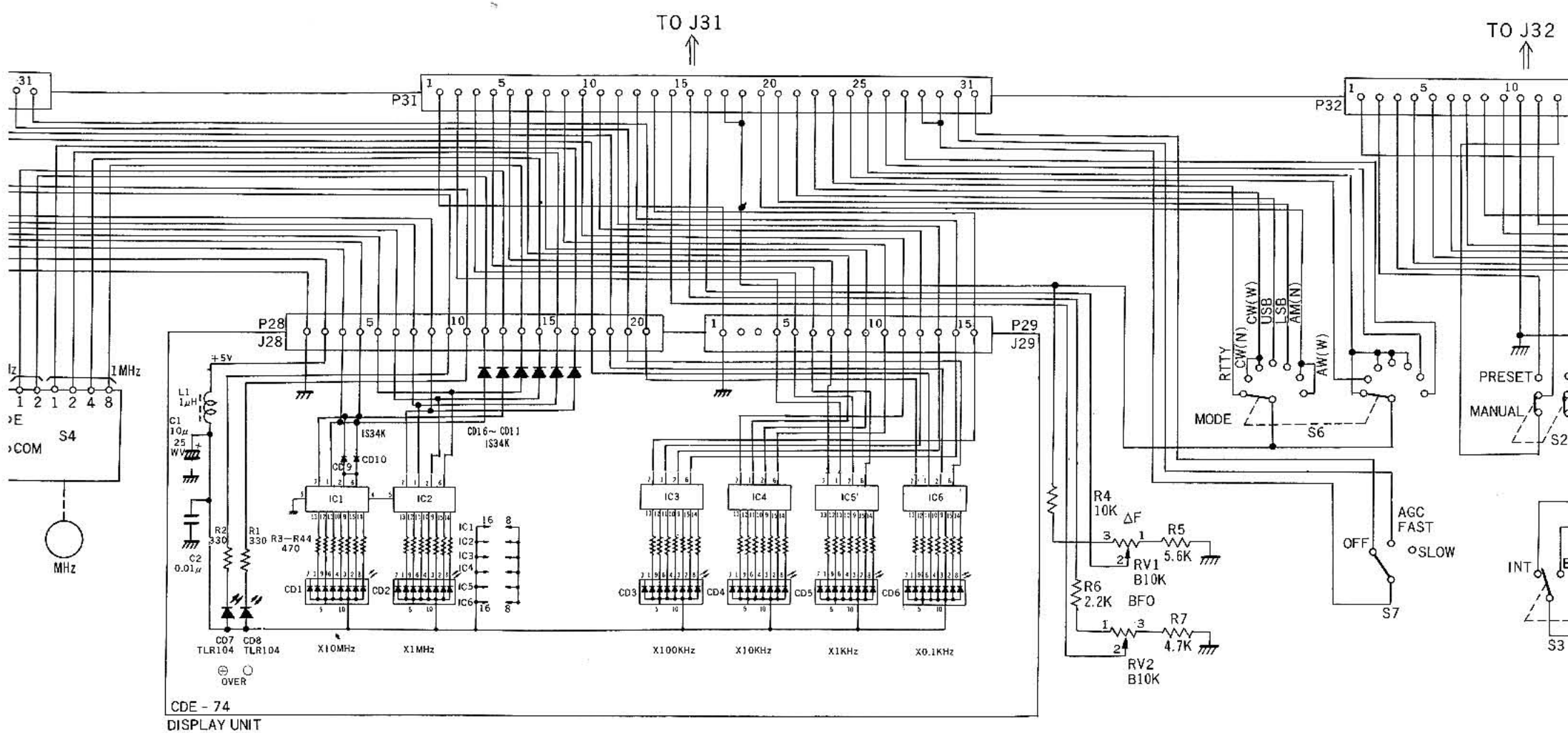
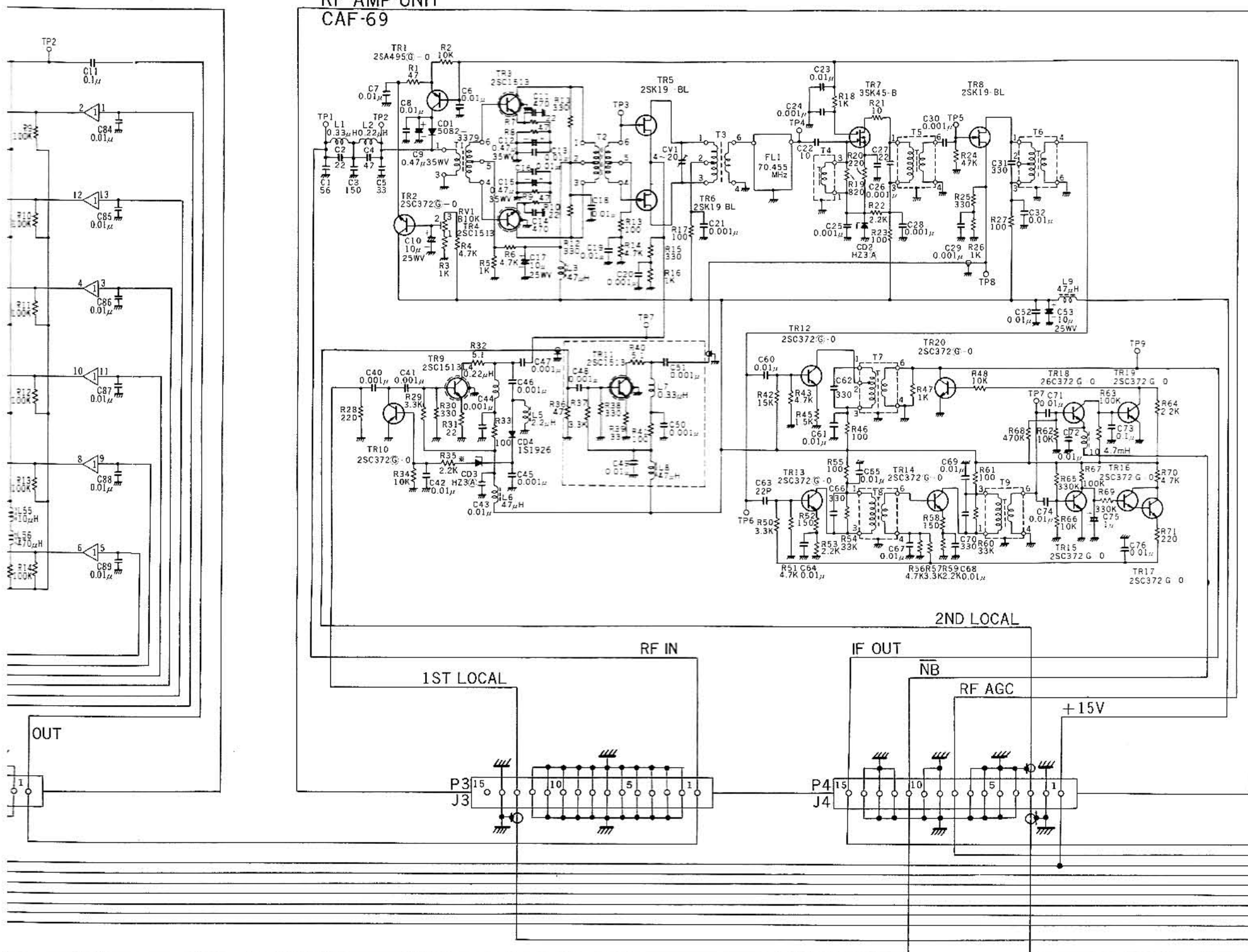


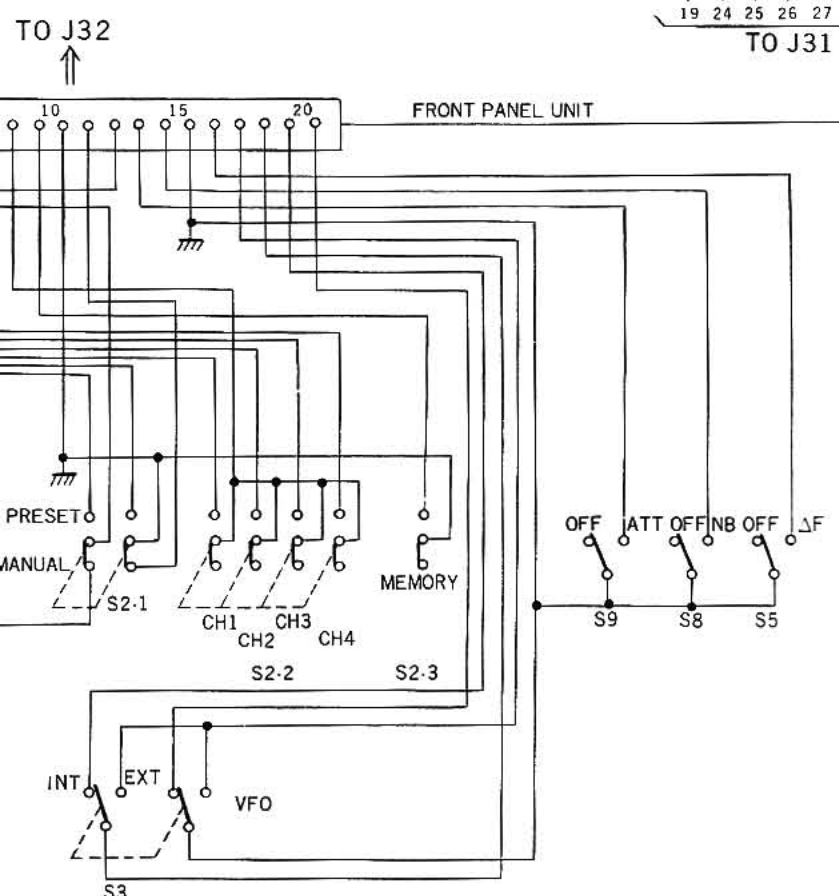
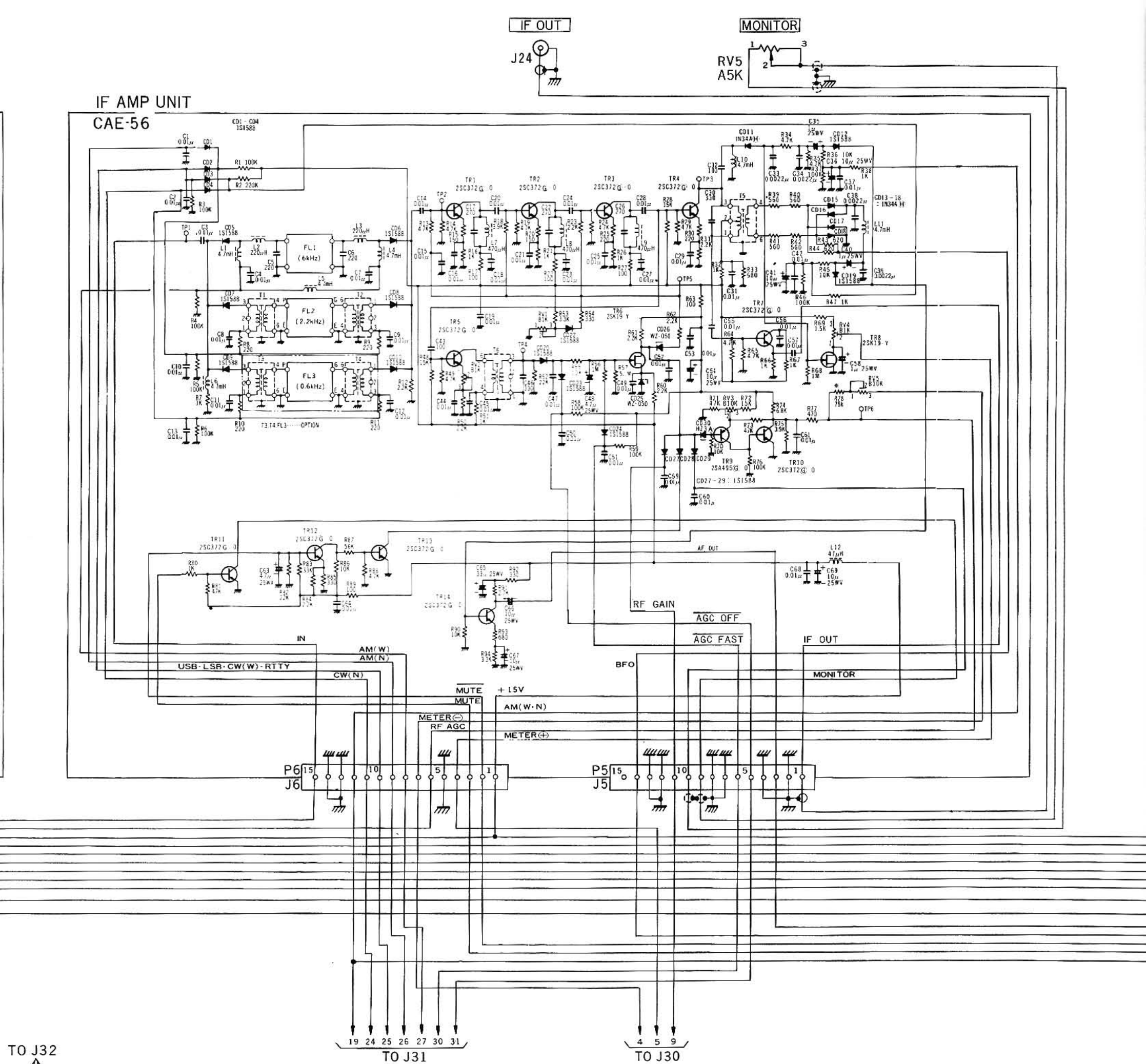
TO J32
1314

TO J30



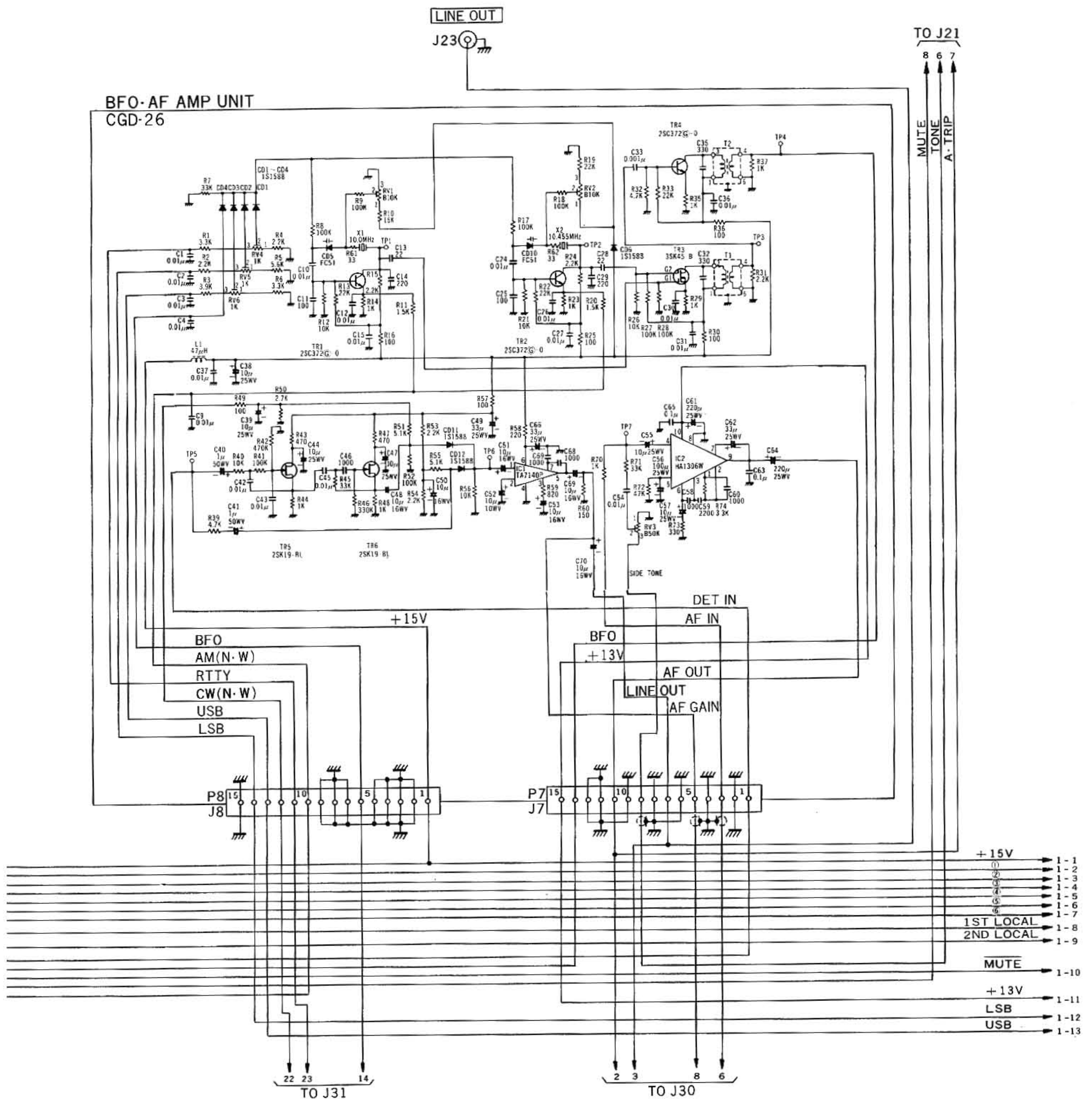
RF AMP UNIT CAF-69



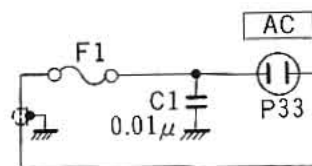


- NOTES 1. Unless otherwise indicated resistances are in ohms capacitances are in micro-micro farads.
2. Values selected in manufacture.

APPENDIX 2 RECEIVING SECTION AND FRONT PANEL CIRCUIT OF MODEL NRD-505 ALL-WAVE RECEIVER

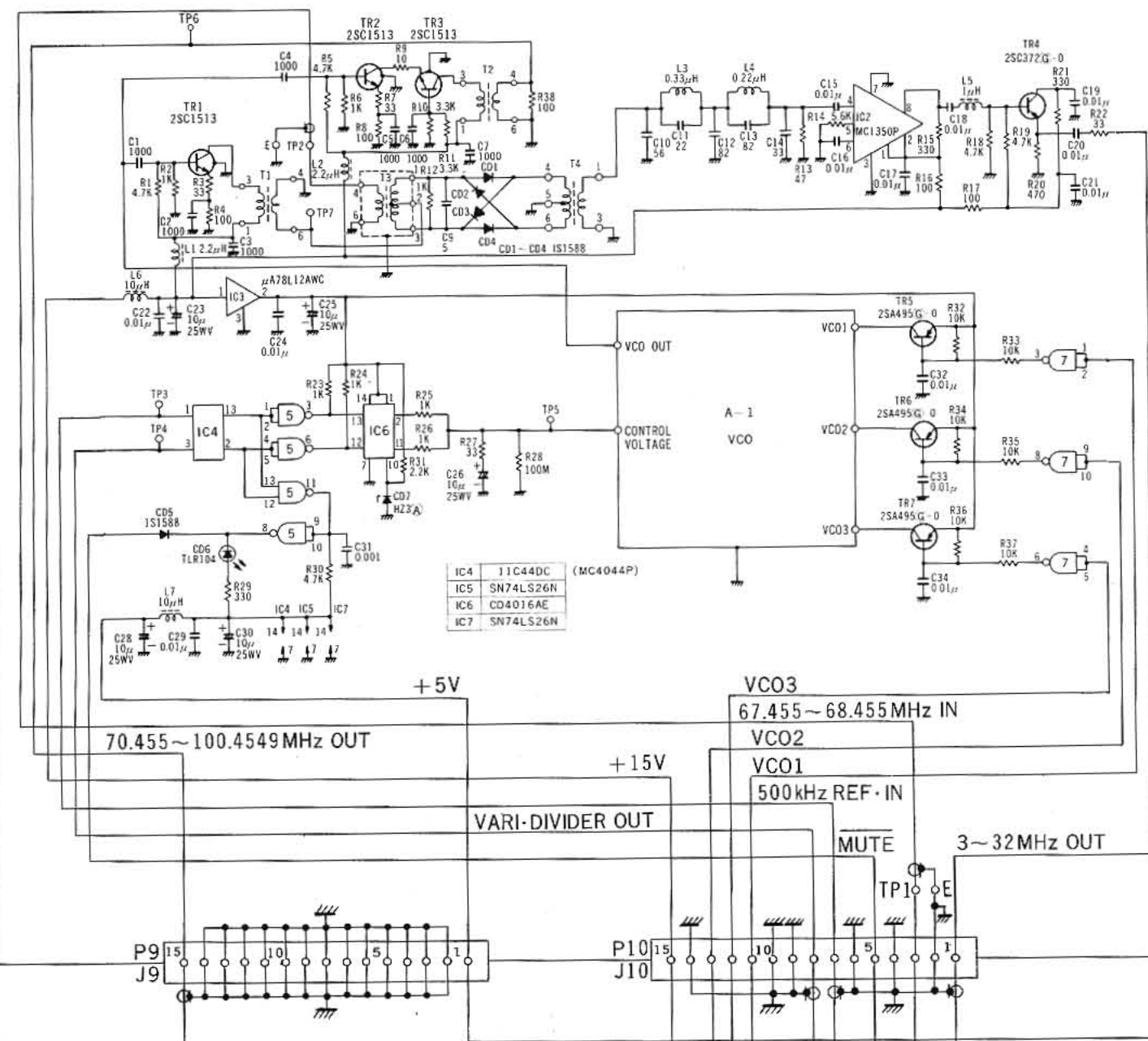


CIRCUIT CONNECTION DIAGRAM
R

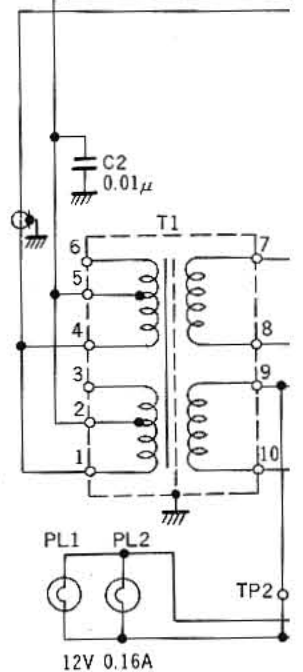


LOOP1 UNIT
CGA-23

LOOP2 I
CGA 24

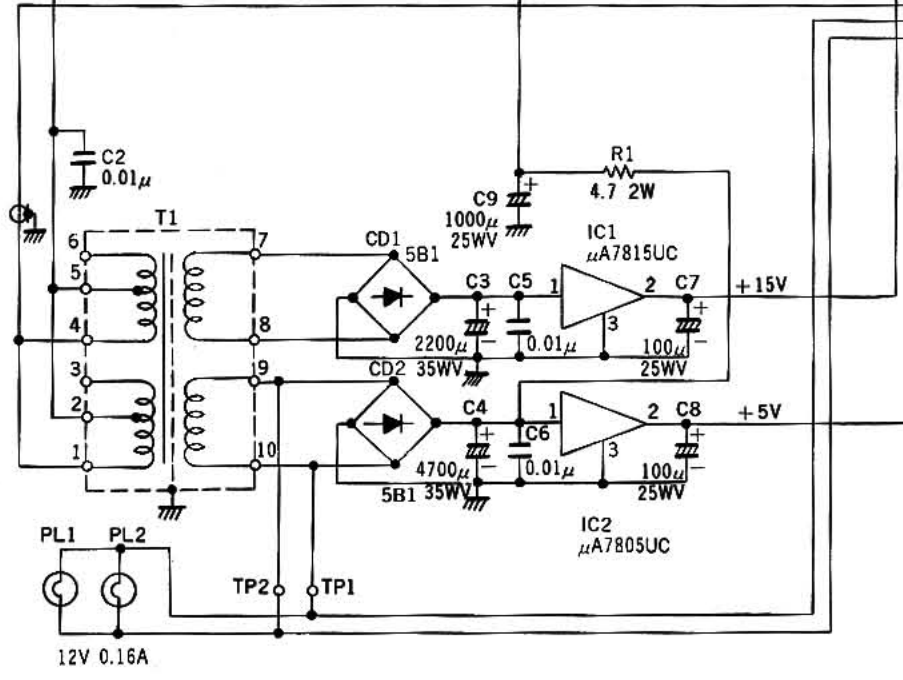
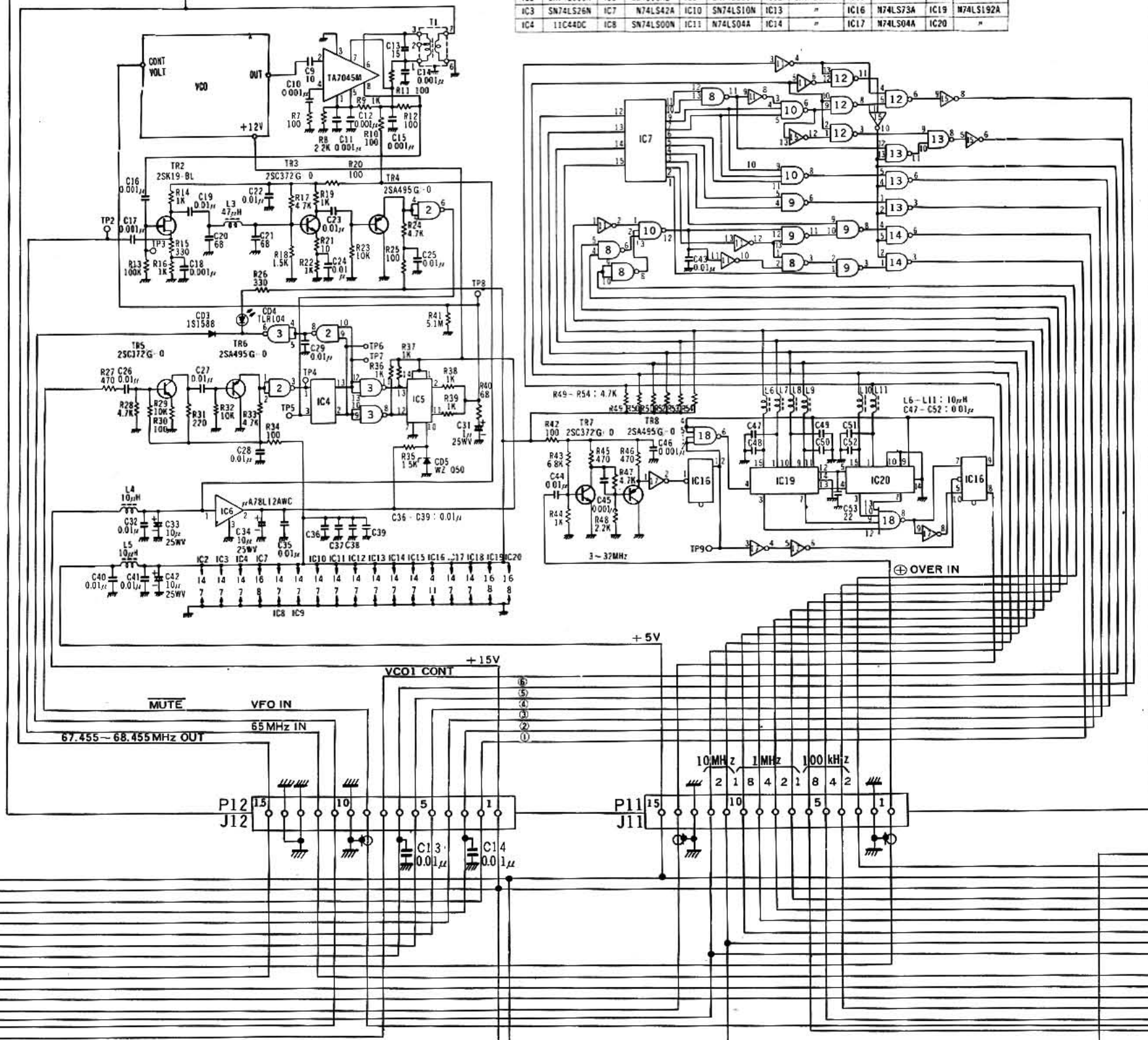


- 1-1 +15V
- 1-2
- 1-3
- 1-4
- 1-5
- 1-6
- 1-7
- 1-8 1ST LOCAL
- 1-9 2ND LOCAL
- 1-10 MUTE
- 1-11 +13V
- 1-12 LSB
- 1-13 USB

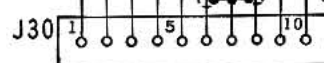


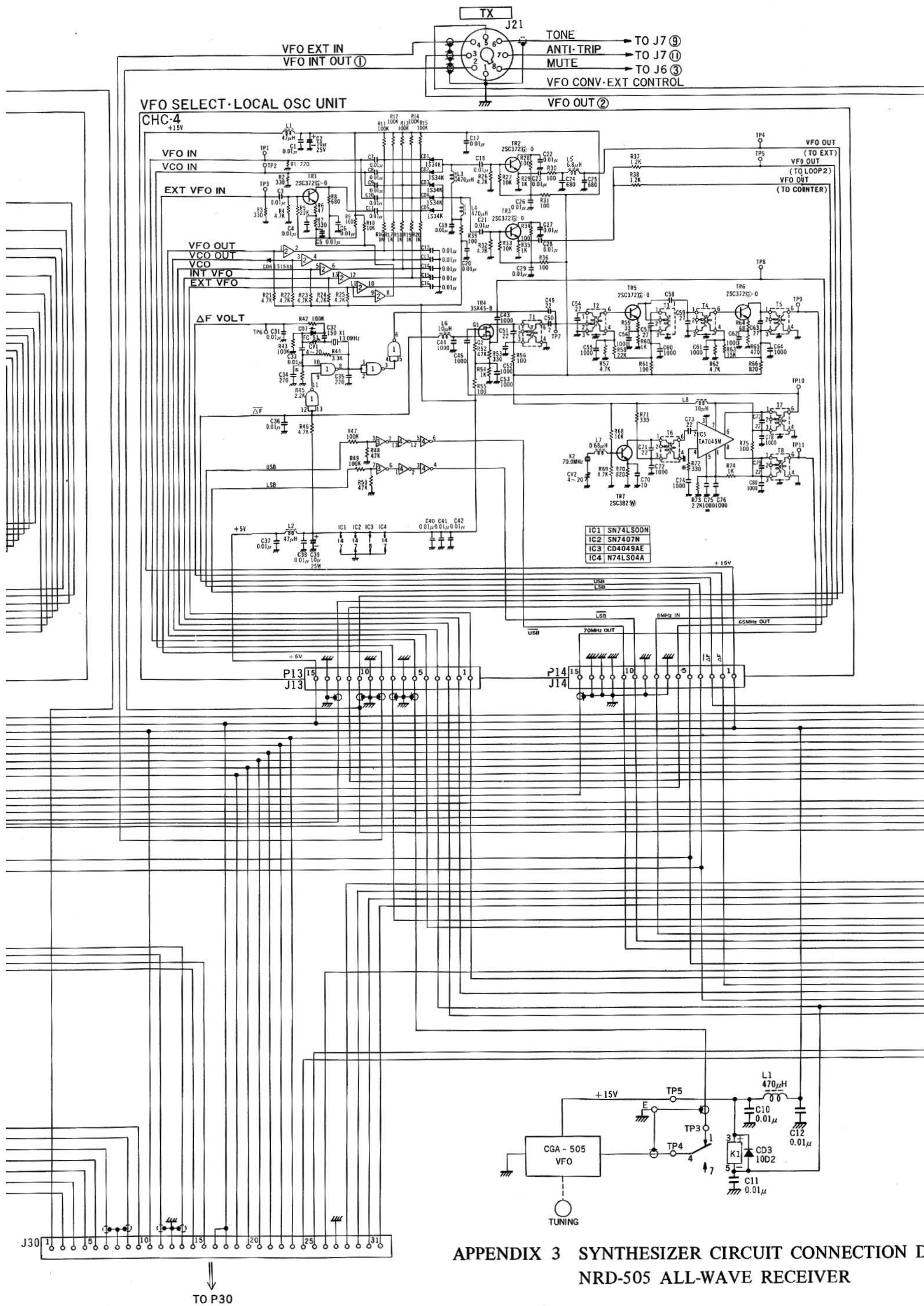
LOOP2 UNIT CGA 24

IC2	SN74LS00N	IC5	CD4016AE	IC9	SN74LS00N	IC12	SN74LS00N	IC15	N74LS04A	IC18	SN74LS20N
IC3	SN74LS26N	IC7	N74LS42A	IC10	SN74LS10N	IC13	"	IC16	N74LS73A	IC19	N74LS192A
IC4	11C44DC	IC8	SN74LS00N	IC11	N74LS04A	IC14	"	IC17	N74LS04A	IC20	"

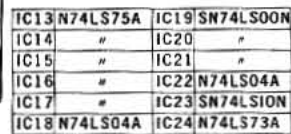


- (RF GAIN) TO J5 ①
- TO J7 ⑤
- (AF GAIN) TO J7 ③
- (S ⊕) TO J6 ④
- (S ⊖) TO J6 ⑦
- (LINE OUT) TO J7 ⑦
- (AF OUT) TO J7 ①





APPENDIX 3 SYNTHESIZER CIRCUIT CONNECTION I
NRD-505 ALL-WAVE RECEIVER



TO P31

J32 ¹ ○ ○ ○

- NOTES 1. Unless otherwise indicated resistances are in ohms
capacitances are in micro-micro farads.
2. Values selected in manufacture.

