

INSTRUCTION MANUAL
FOR
MODEL JST-100 HF TRANSCEIVER



Japan Radio Co., Ltd.

CHAPTER 7

BLOCKDIAGRAM AND CIRCUIT DIAGRAMS

FIGURE 7.1	BLOCK DIAGRAM
FIGURE 7.2	CFL-175 BPF UNIT CIRCUIT DIAGRAM
FIGURE 7.3	CAF-180 RF AMP UNIT CIRCUIT DIAGRAM
FIGURE 7.4	CAE-137 IF AMP UNIT CIRCUIT DIAGRAM
FIGURE 7.5	CAB-259 AF AMP UNIT CIRCUIT DIAGRAM
FIGURE 7.6	CAH-157 100W PA UNIT CIRCUIT DIAGRAM
FIGURE 7.7	CAH-158 10W PA UNIT CIRCUIT DIAGRAM
FIGURE 7.8	CMB-63 OUTPUT UNIT CIRCUIT DIAGRAM
FIGURE 7.9	CGA-94 LOOP 1 UNIT CIRCUIT DIAGRAM (1/2)
FIGURE 7.10	CGA-94 LOOP 1 UNIT CIRCUIT DIAGRAM (2/2)
FIGURE 7.11	CGA-95 LOOP 2 UNIT CIRCUIT DIAGRAM
FIGURE 7.12	CGD-76 BFO UNIT CIRCUIT DIAGRAM
FIGURE 7.13	CDC-236 CPU UNIT CIRCUIT DIAGRAM
FIGURE 7.14	CFQ-1168 FRONT PANEL CIRCUIT DIAGRAM
FIGURE 7.15	CWB-232 MOTHER BOARD CIRCUIT DIAGRAM

Circuit and components subject to change for an improvement without notice.

FIGURE 7.2 CFL-175 BPF UNIT CIRCUIT DIAGRAM

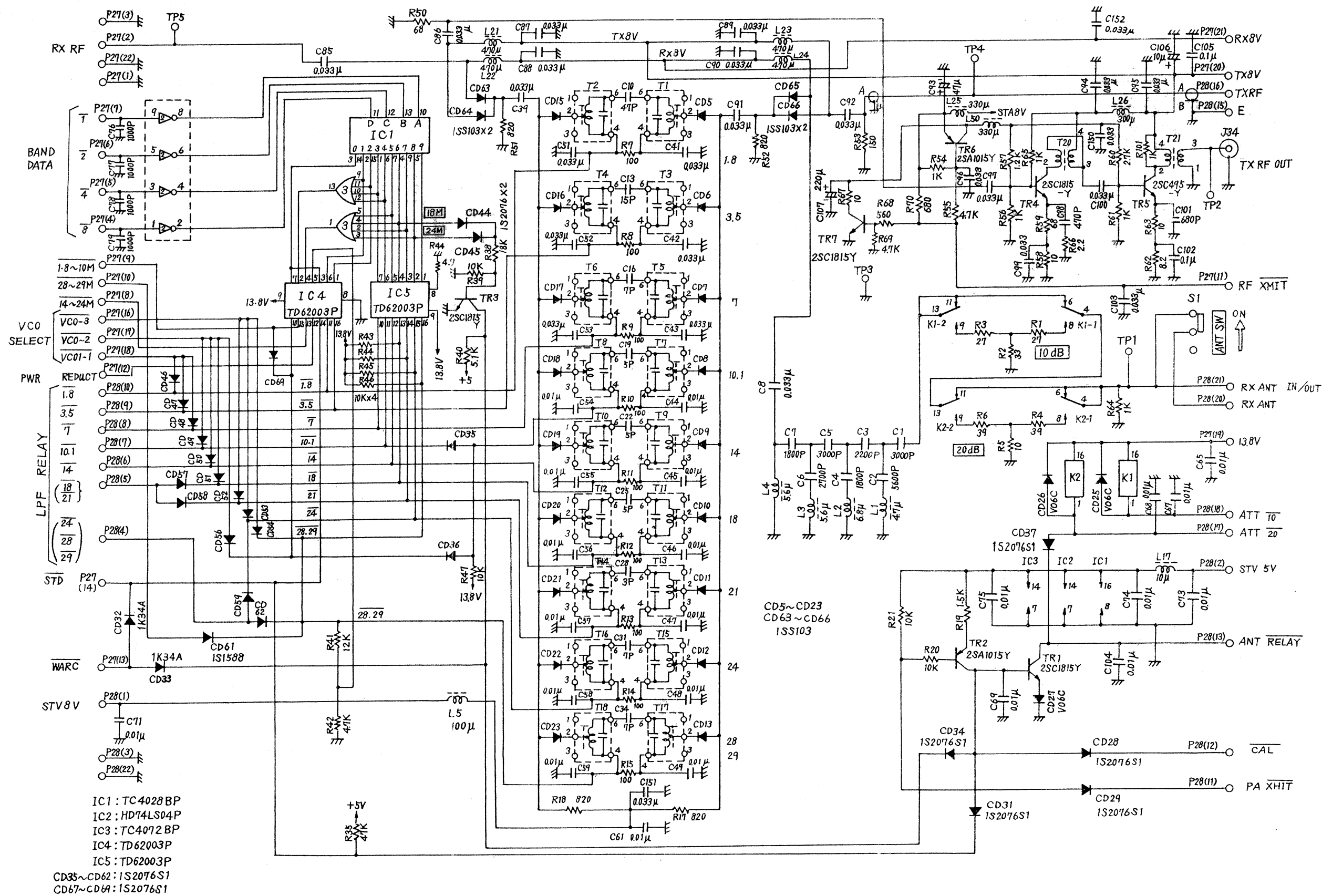


FIGURE 7.3 CAF-180 RF AMP UNIT CIRCUIT DIAGRAM

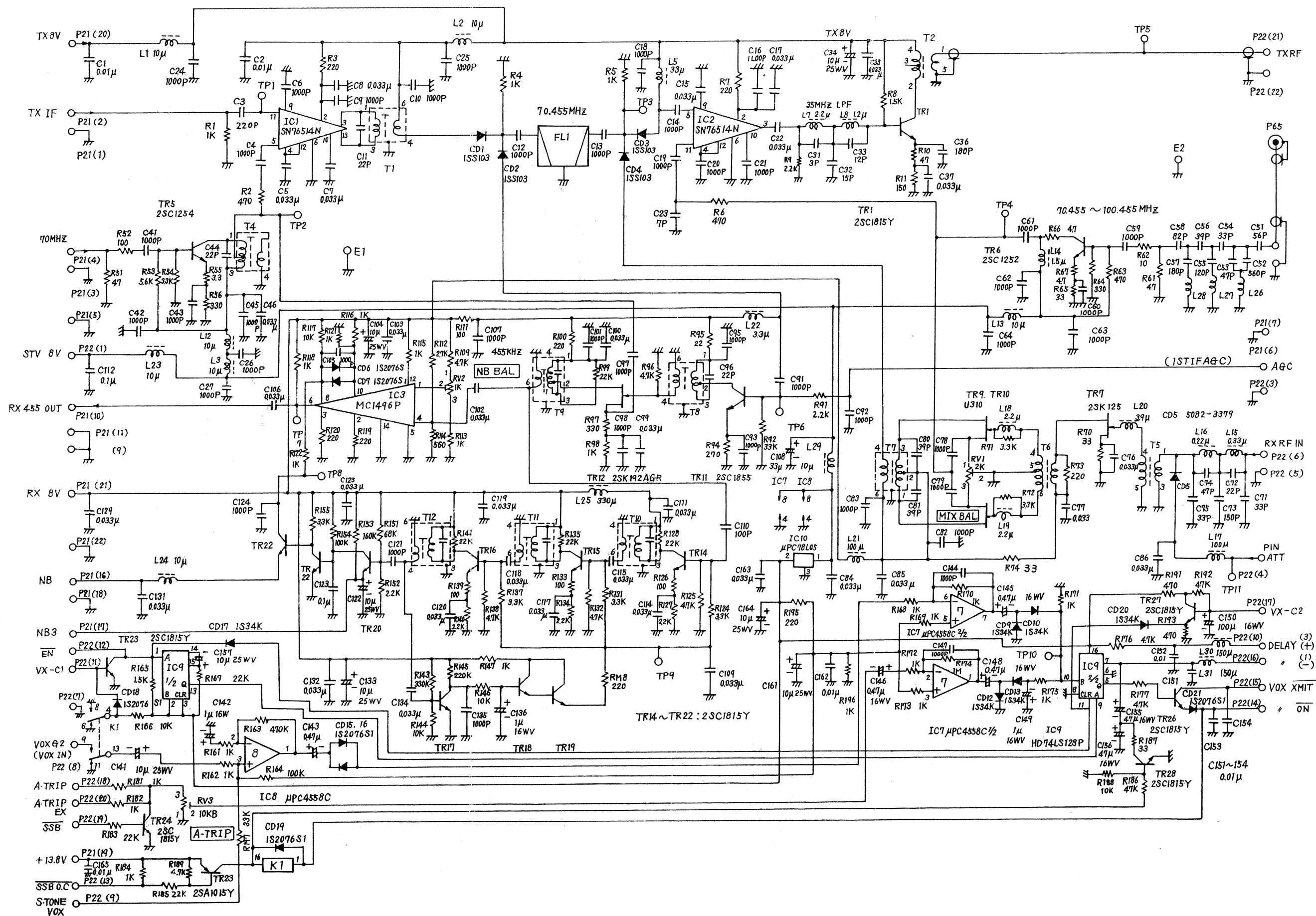
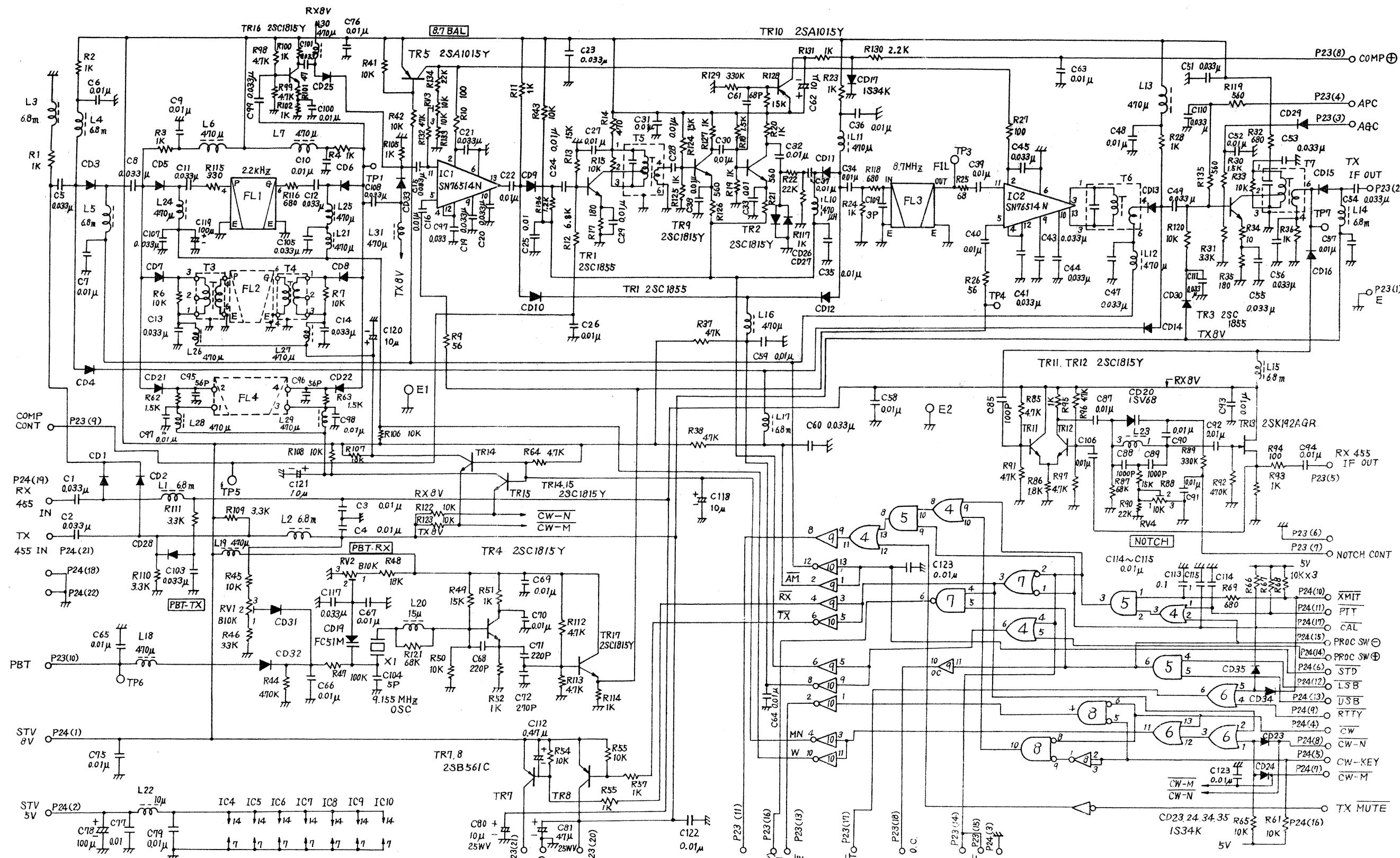


FIGURE 7.4 CAE-137 IF AMP UNIT CIRCUIT DIAGRAM



IC4: HD74LS32P IC8: HD74LS02P
 IC5: " LS08P IC9: HD7417P
 IC6: " LS32P IC10: HD7416P
 IC7: " LS00P

FIGURE 7.5 CAB-259 AF AMP UNIT CIRCUIT DIAGRAM

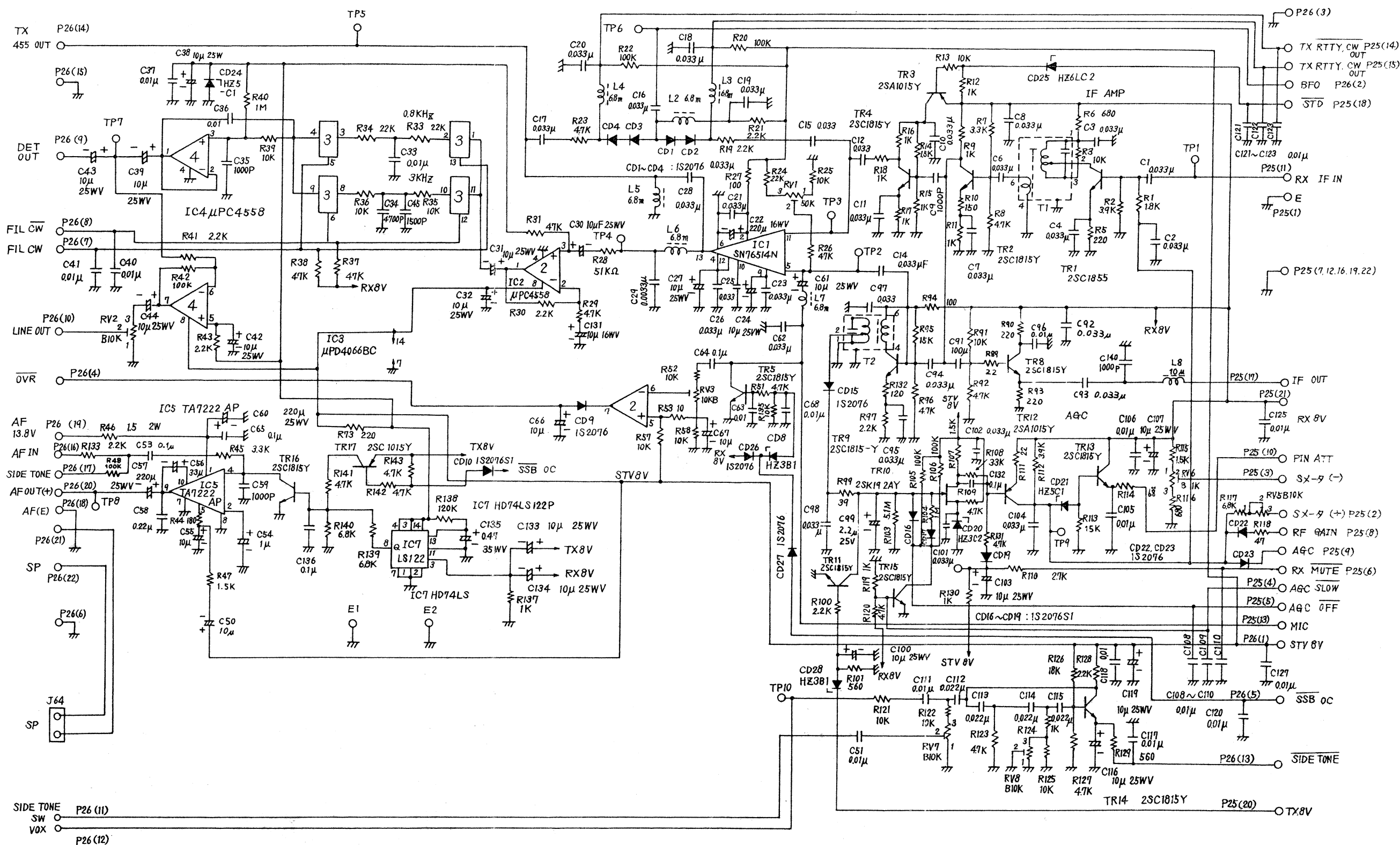
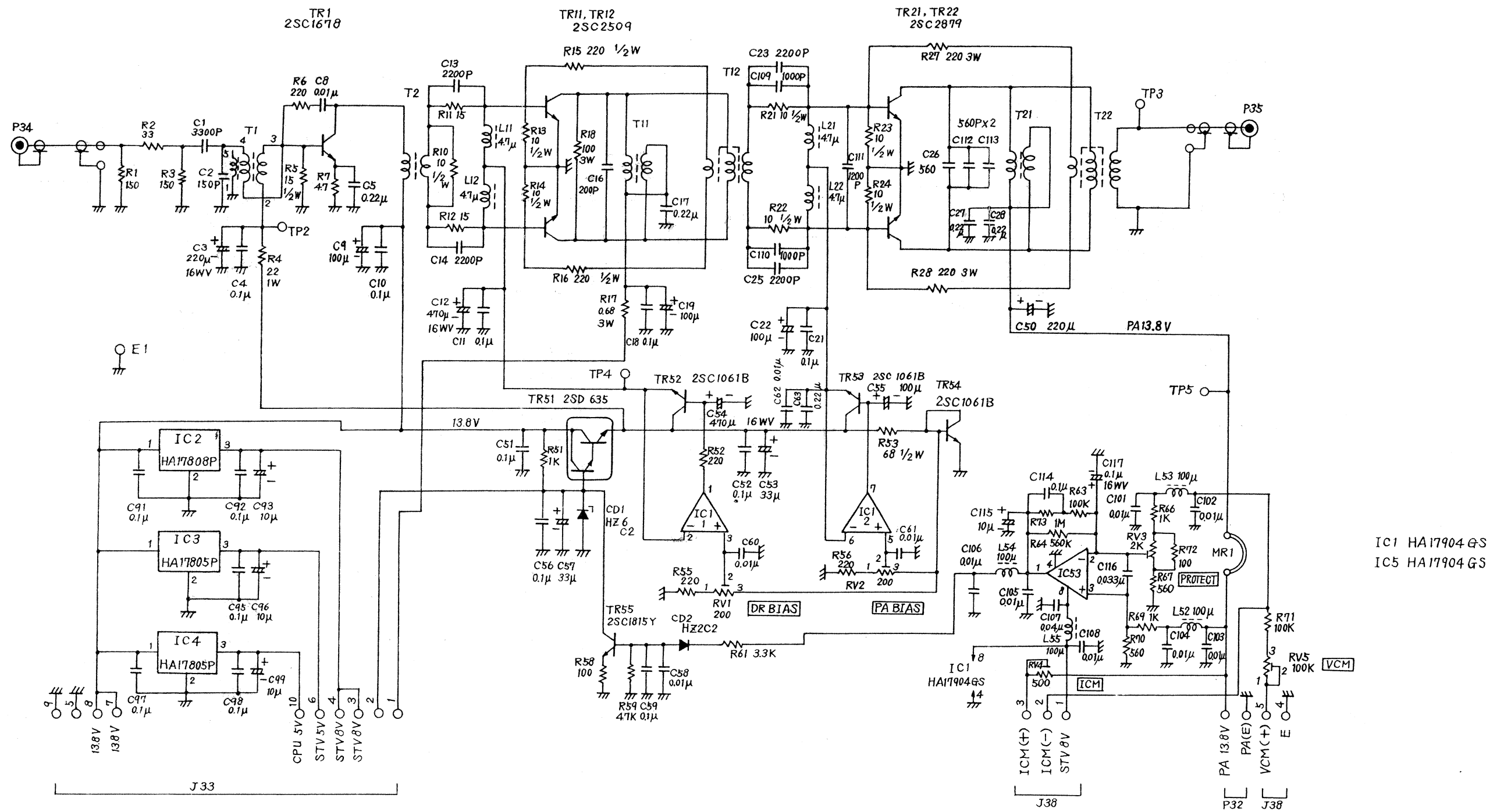


FIGURE 7.6 CAH-157 100W PA UNIT CIRCUIT DIAGRAM



CAH-158 10W PA UNIT CIRCUIT DIAGRAM

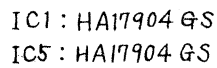


FIGURE 7.8 CMB-63 OUTPUT UNIT CIRCUIT DIAGRAM

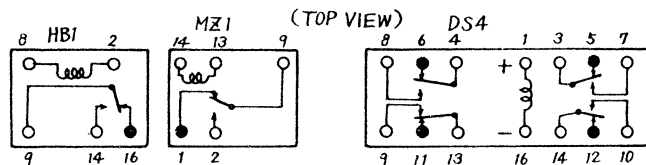
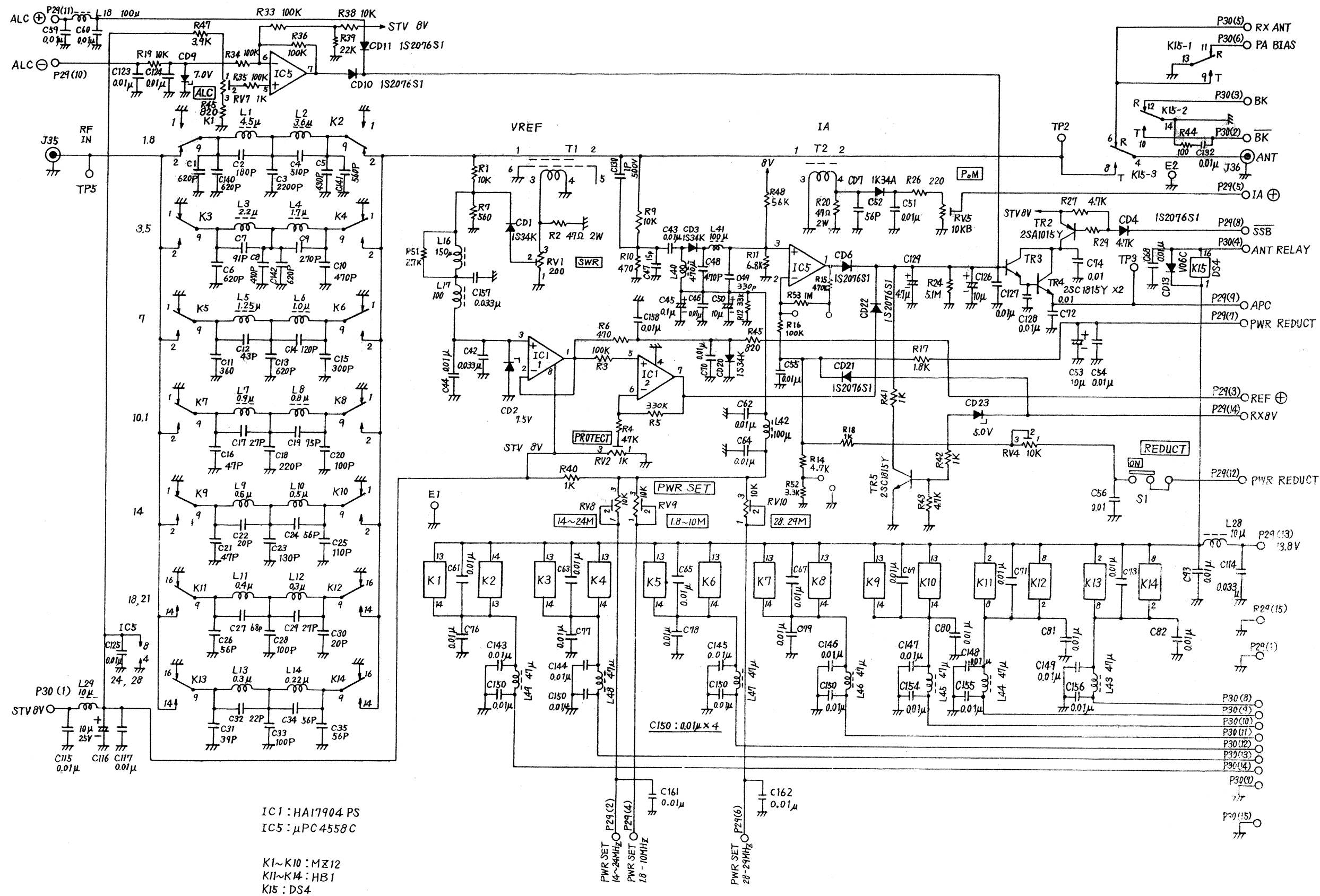
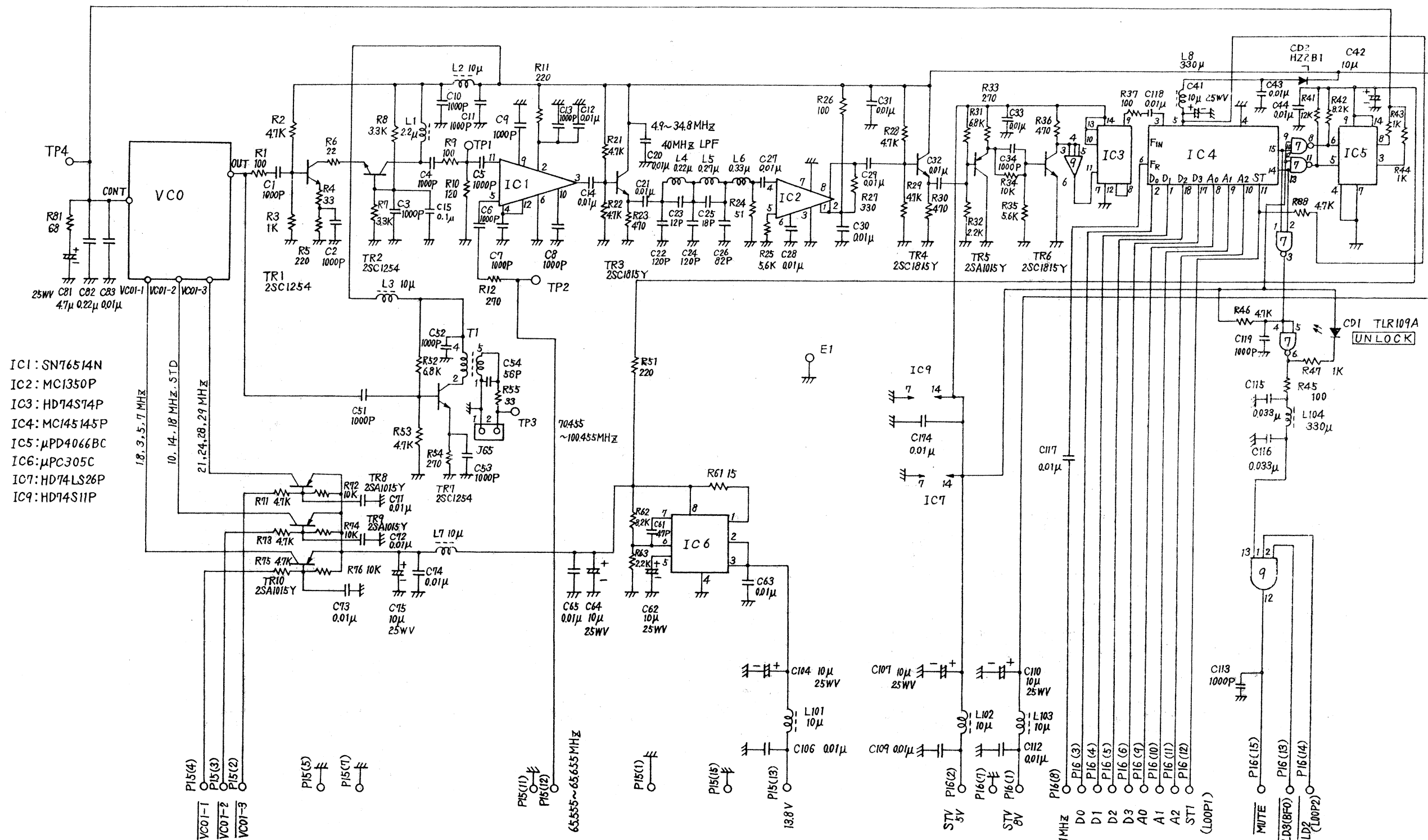


FIGURE 7.9 CGA-94 LOOP 1 UNIT CIRCUIT DIAGRAM (1/2)



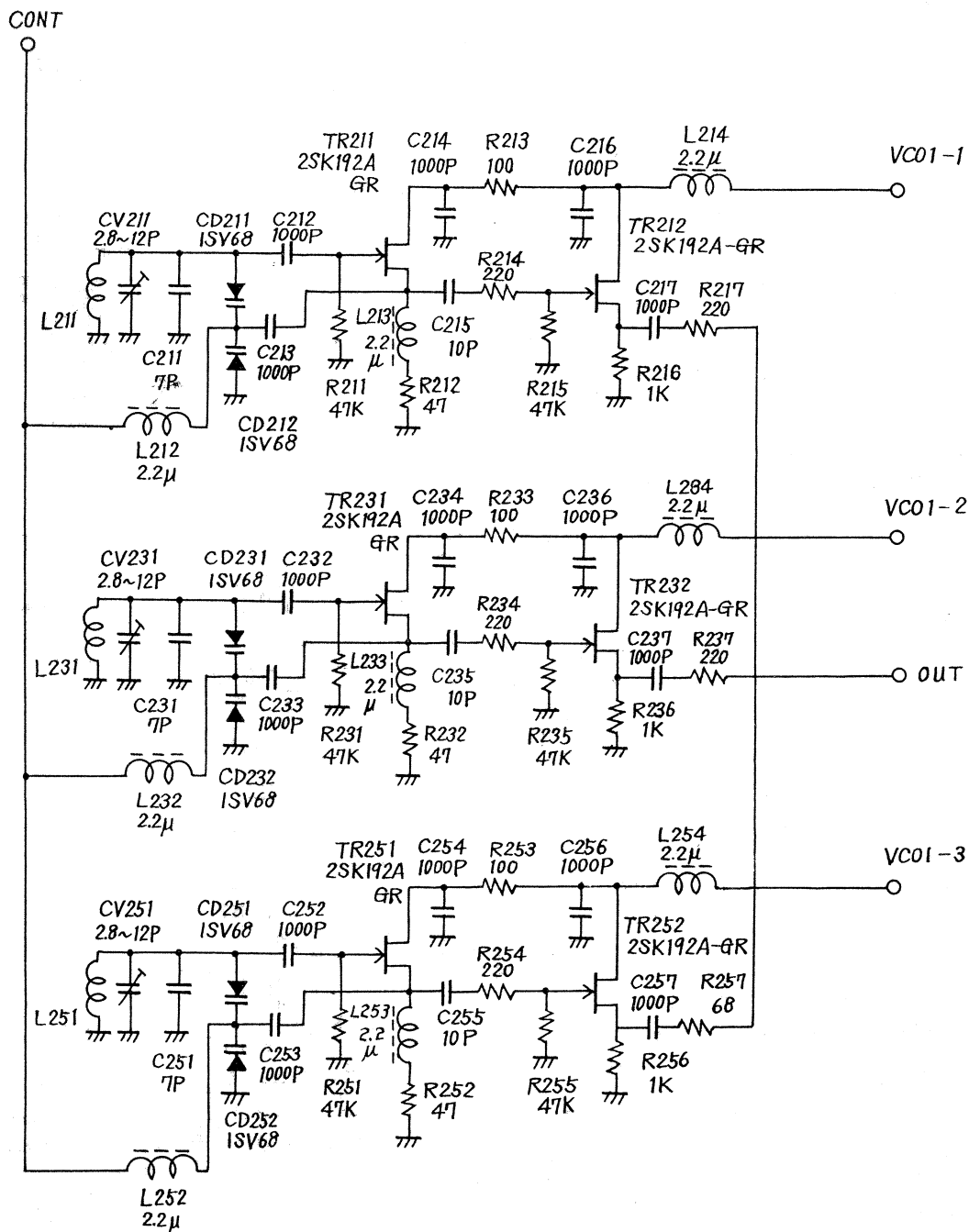


FIGURE 7.10 CGA-94 LOOP 1 UNIT CIRCUIT DIAGRAM (2/2)

FIGURE 7.11 CGA-95 LOOP 2 UNIT CIRCUIT DIAGRAM

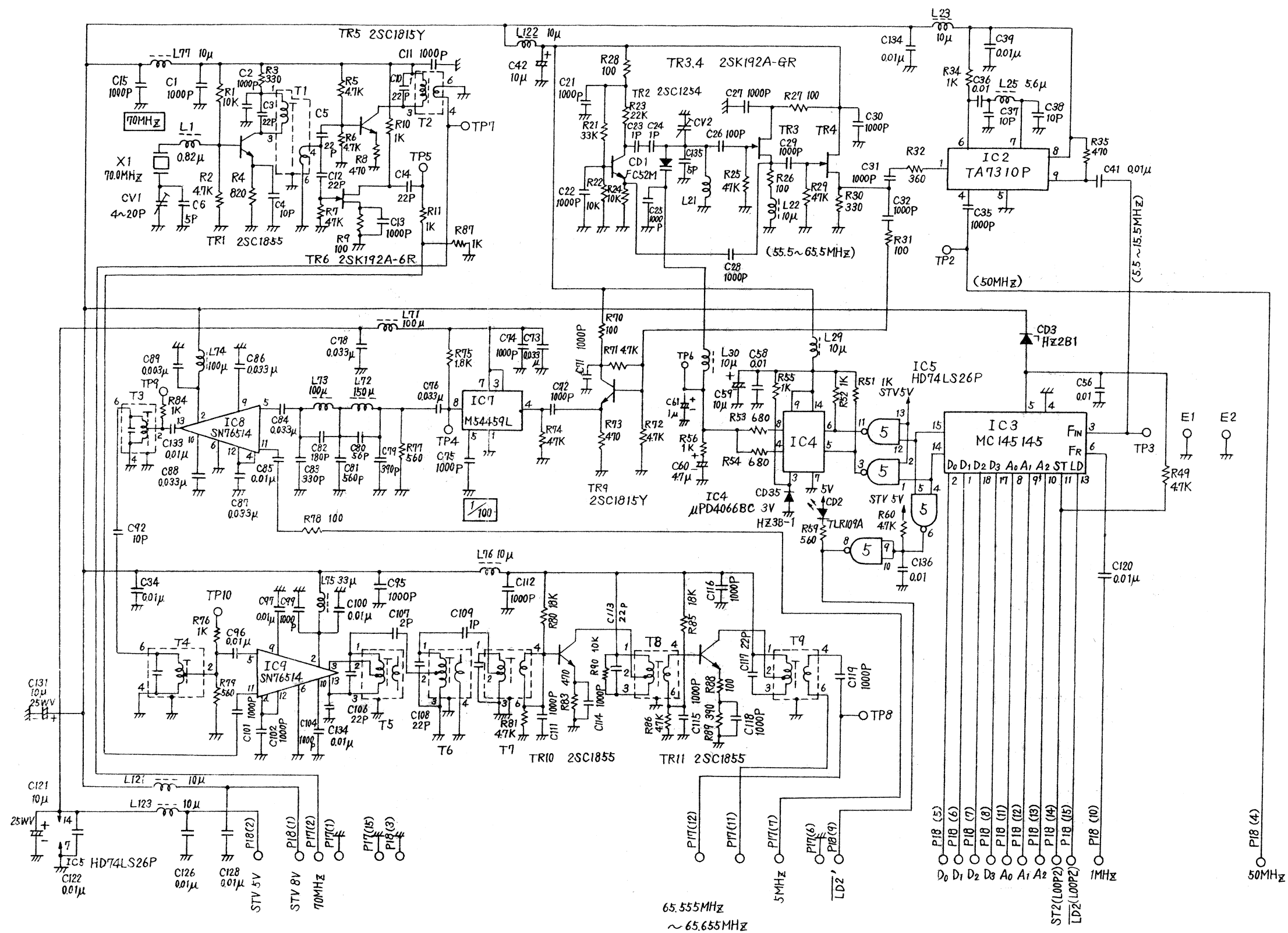
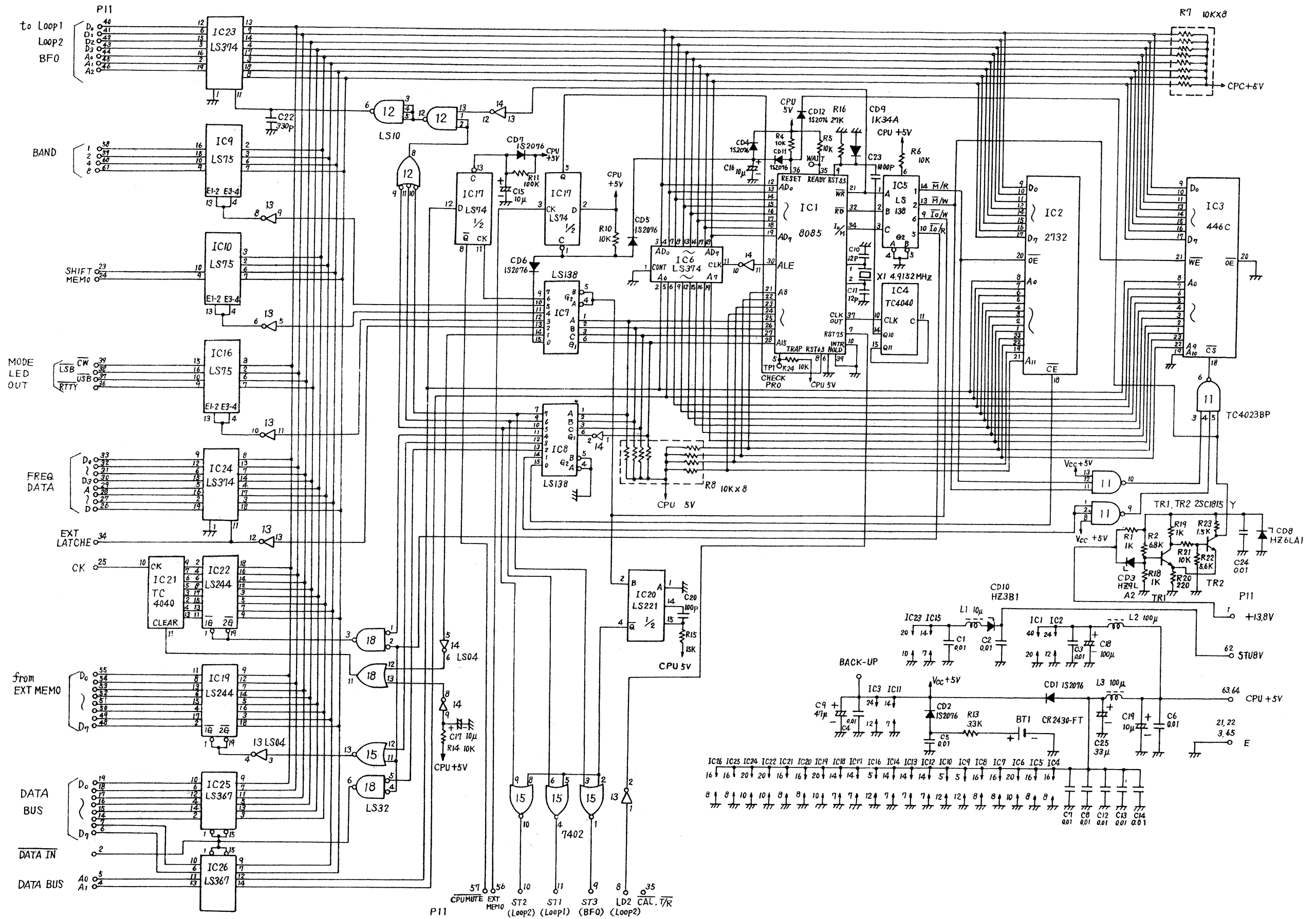


FIGURE 7.13 CDC-236 CPU UNIT CIRCUIT DIAGRAM



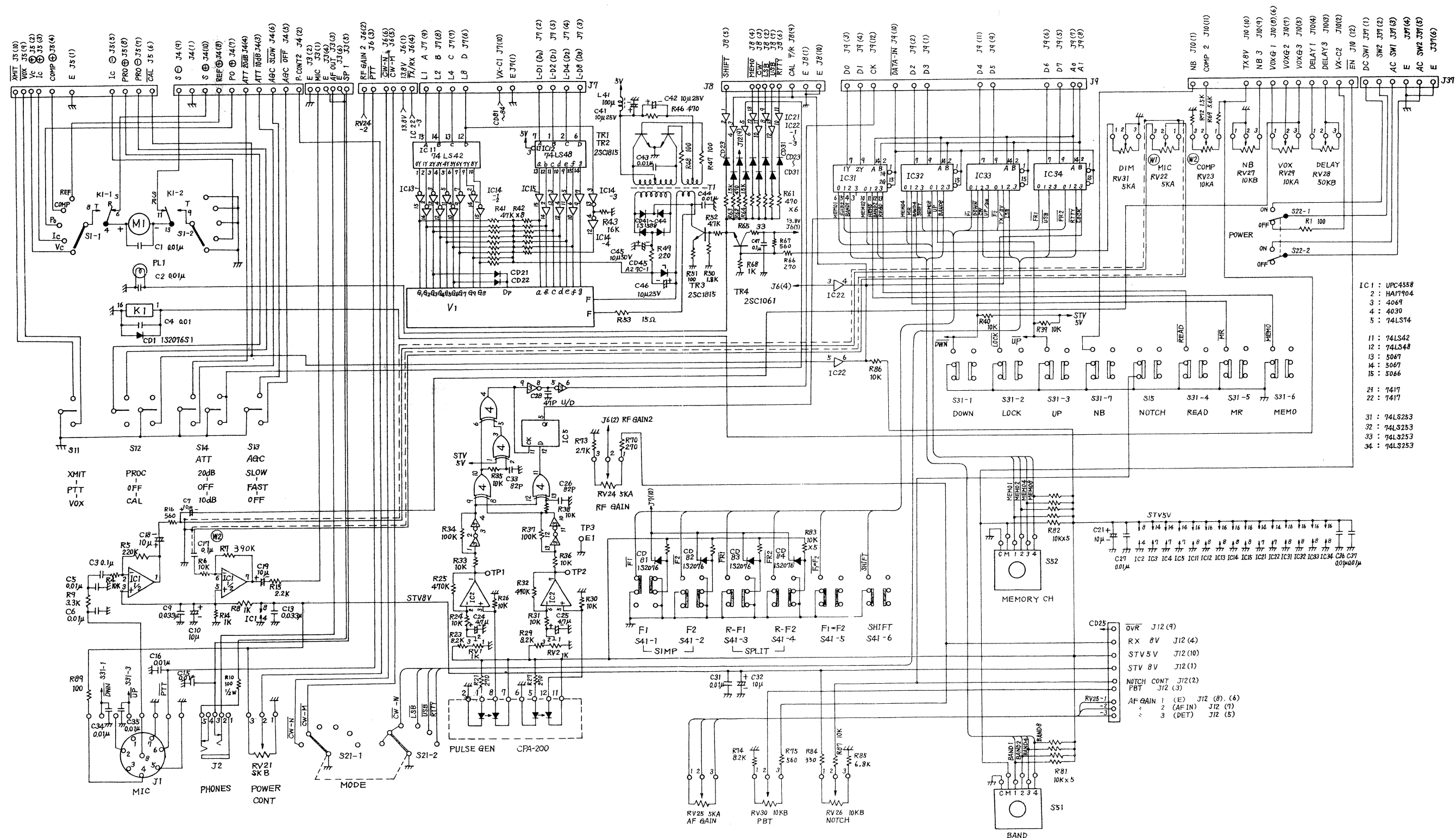


FIGURE 7.14 CFQ-1168 FRONT PANEL CIRCUIT DIAGRAM

